

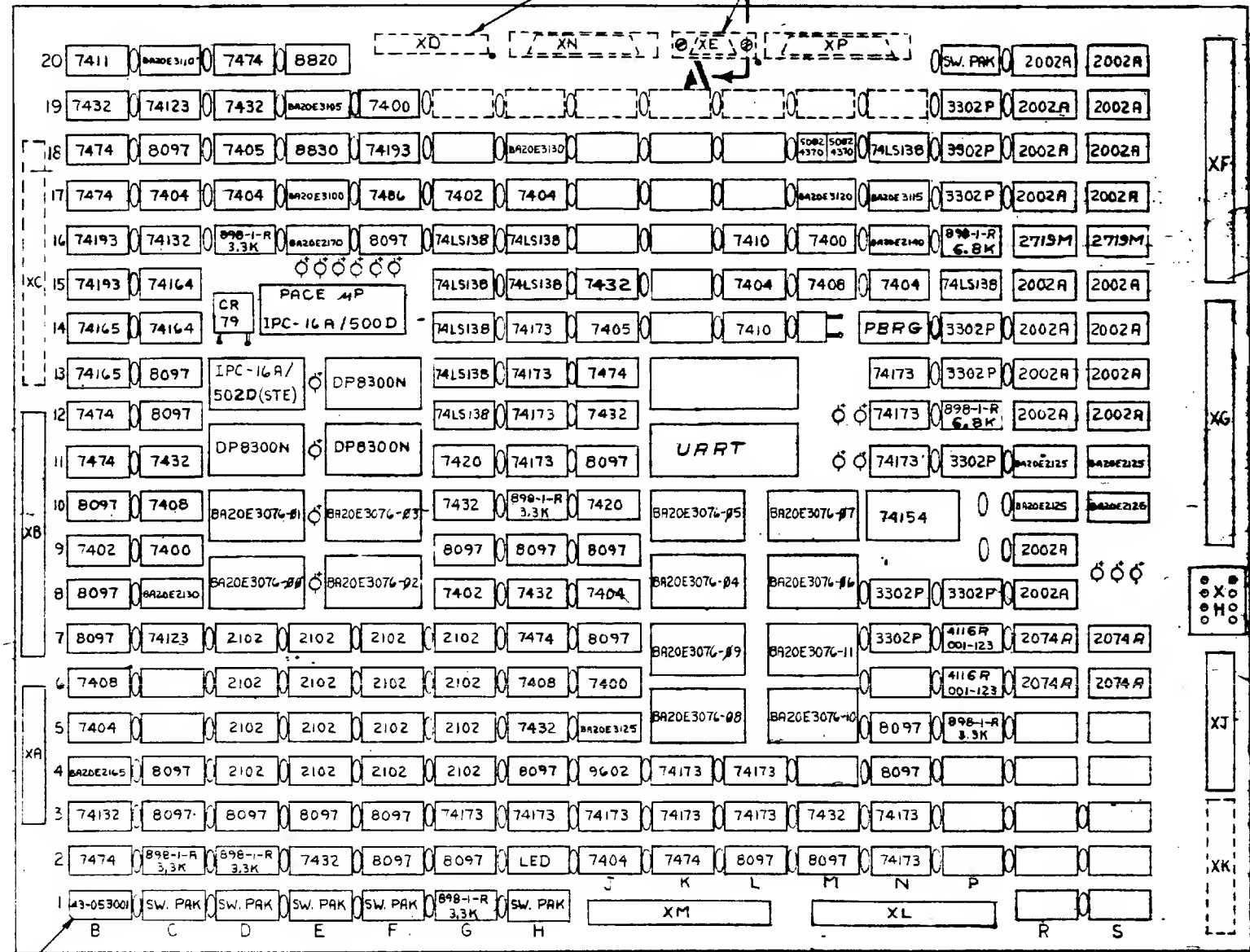
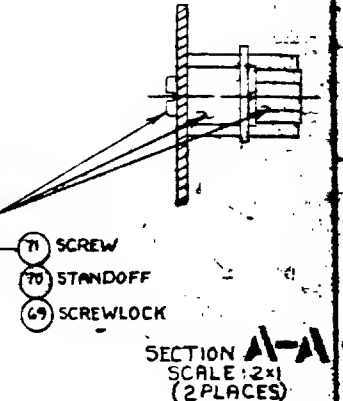
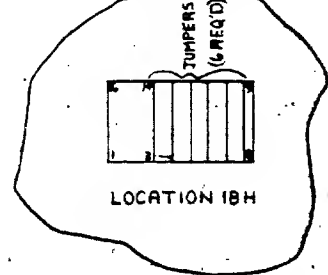
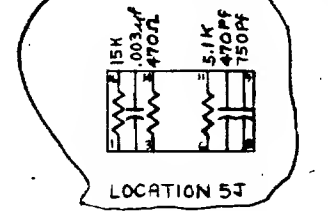
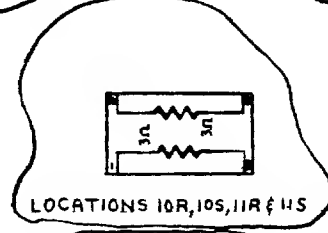
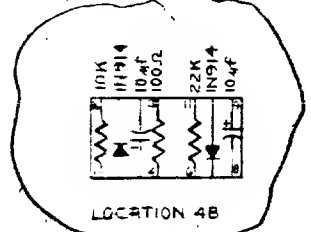
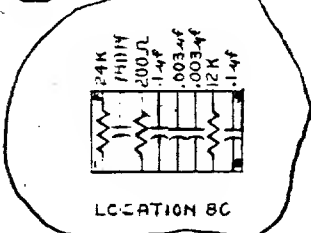
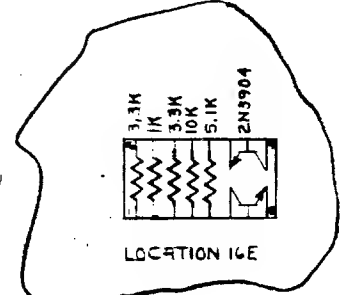
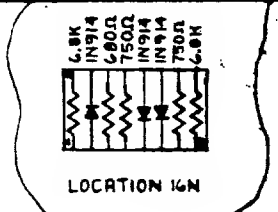
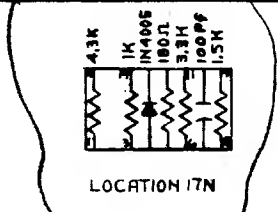
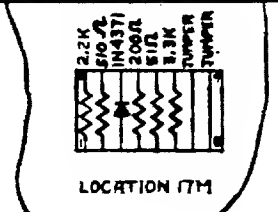
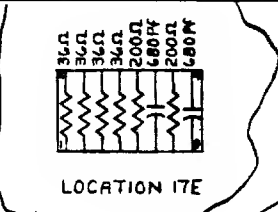
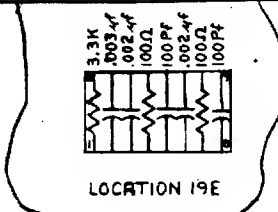
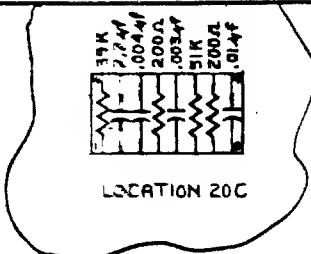
CV Digitizer (CVD)-Series 3

Logic Diagrams

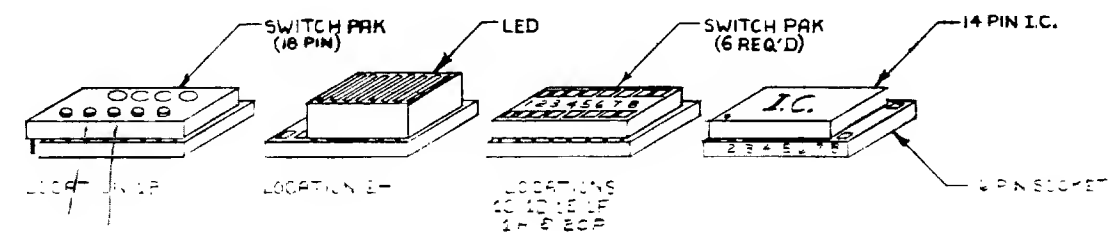
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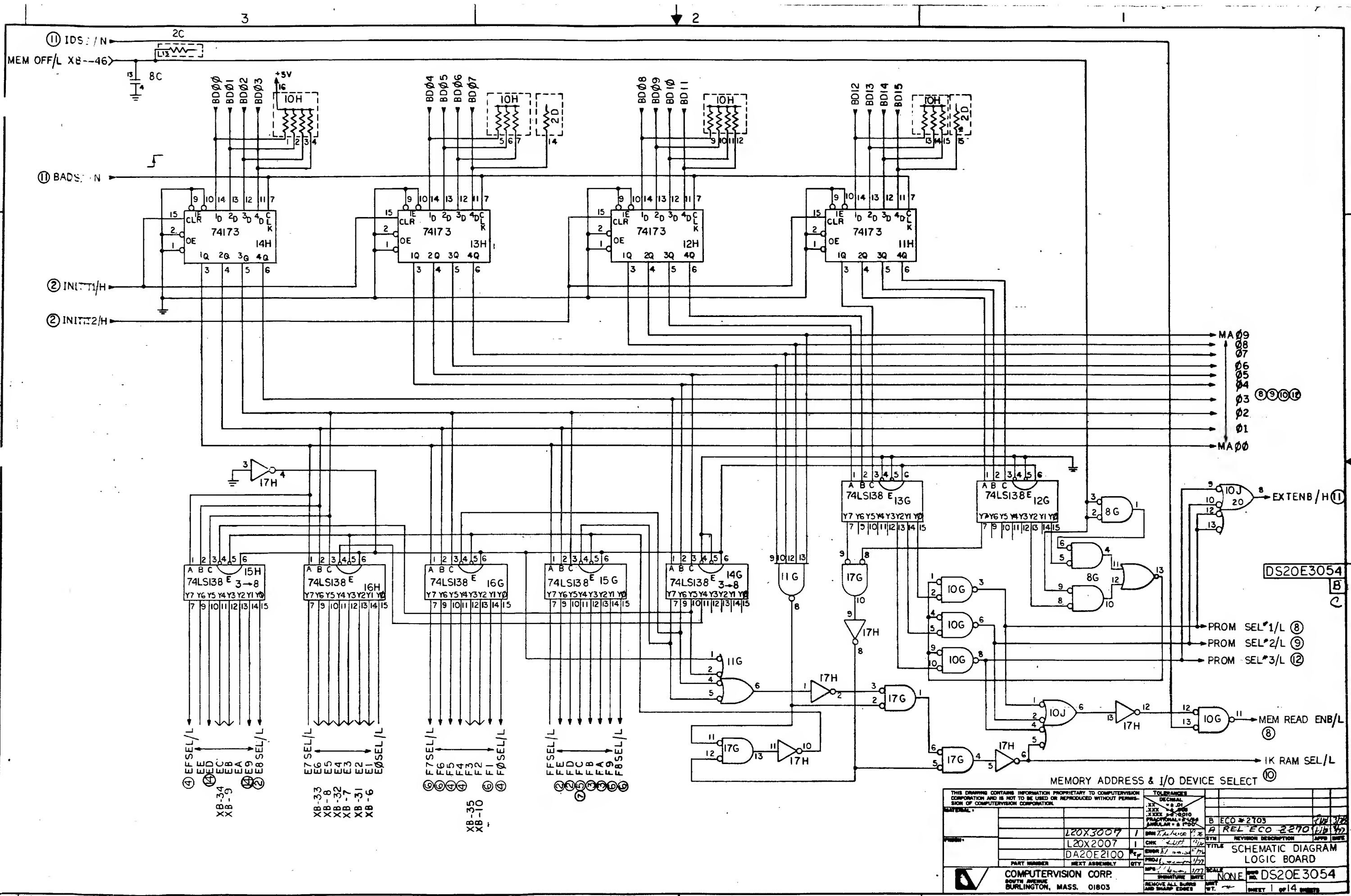
REVISIONS				COMPUTERVISION CORPORATION		L20X3007		B REV.
SYM	ECO NO.	APPR				TITLE		
A	REL. 2270	3/77	Ruth			CVD ELECTRONIC		
B	ECQ 2439	8/77	Lent			DOCUMENTATION PACKAGE		
				L20X3003	1	MODULE CVD 3000		
				NEXT ASSY	QTY	DRN <i>Gibson</i>	SHT 1 OF 1	
ITEM	PART NO.	REV	QTY	DESCRIPTION (REF DES.)		MANUFACTURER		
1	L20X3007	A	REF	CVD ELECT. DOCUM. PKG		NO DRAWING		
2								
3								
4								
5								
6								
7								
8	DA20E3050	A	1	LOGIC BOARD ASSY				
9	DS20E3054	A	1	LOGIC BOARD SCHEMATIC				
10	DS20E3061	A	1	ANALOG BOARD SCHEMATIC				
11	CS20E2068	D	1	PUCK BOARD SCHEMATIC				
12	CS20E2236	A	1	PEN BOARDS SCHEMATIC				
13	DS20E2046	B	1	MESSAGE UNIT SCHEMATIC				
14	DS20E3002	A	1	SYSTEM INTERCONN. DIAG.				
15								
16	DS20E2196	A	1	BACK LIGHTING SCHEMATIC				
17								
18	BS20E3097	A	1	CVD TIMING BOARD SCHEMATIC				
19								
20								
21								
22								
23								
24								
25								

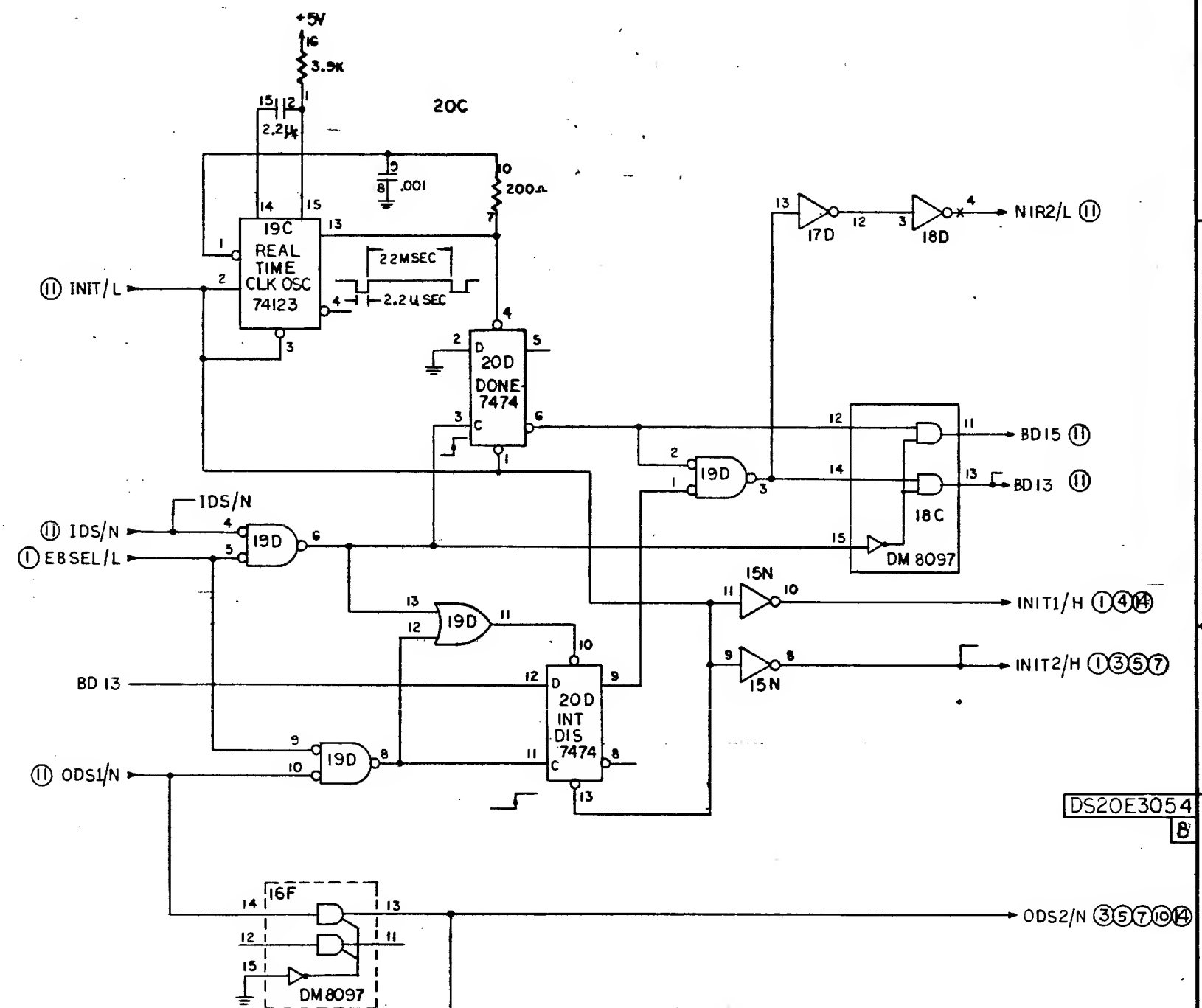
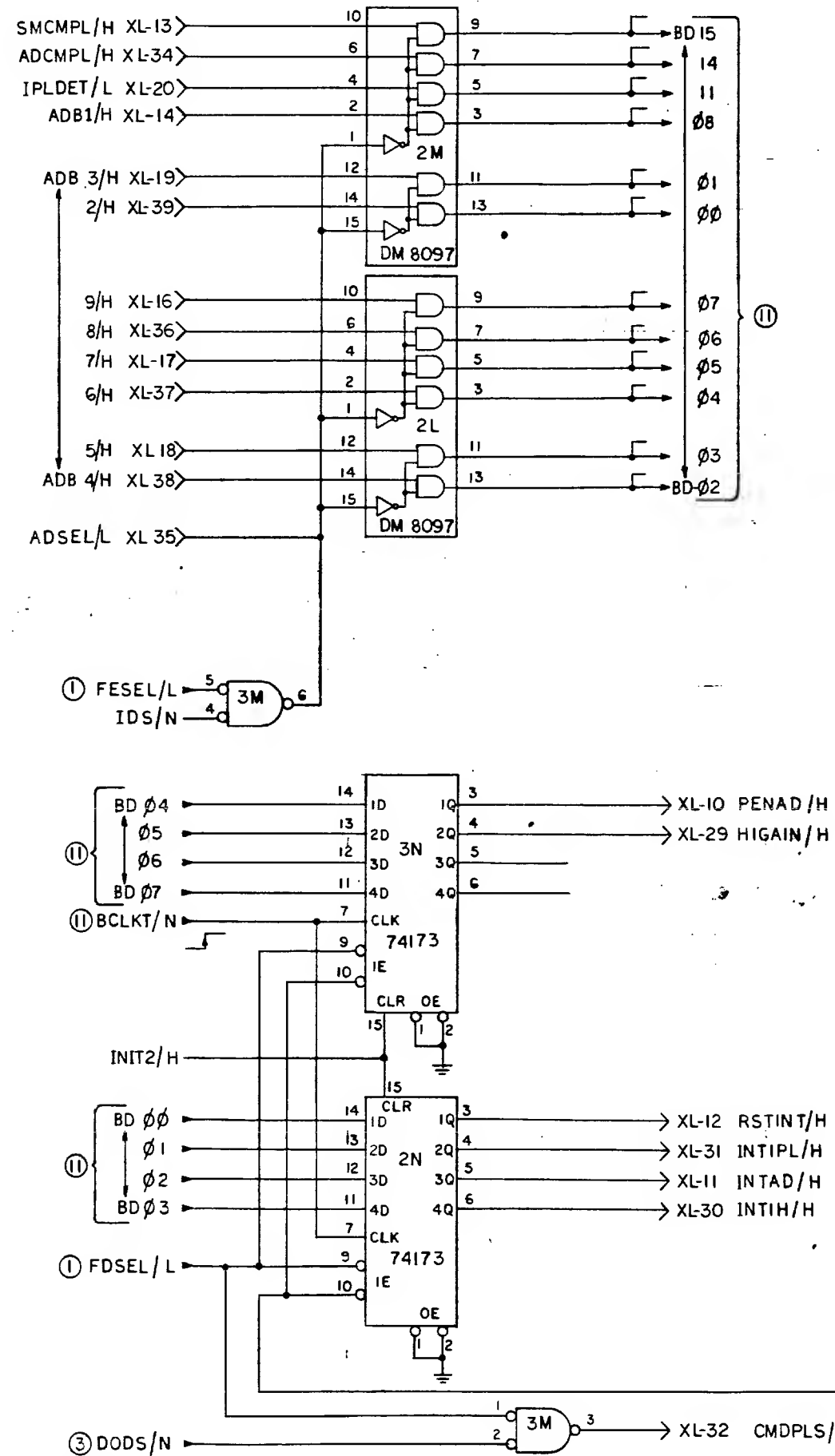


NOTES:
1 REMOVE SOLDER MASK FROM AREAS INDICATED. INSTALL CONNECTORS XD & XE ON FAR SIDE OF BOARD CHANGING SOLDER.
2 LOCATE PIN 1 IN CORNER, TYPICAL FOR 14 PIN IC.
3 REMOVE SOLDER MASK FROM CONNECTOR XD FOR KEYING BEFORE SOLDERING IN PLACE.



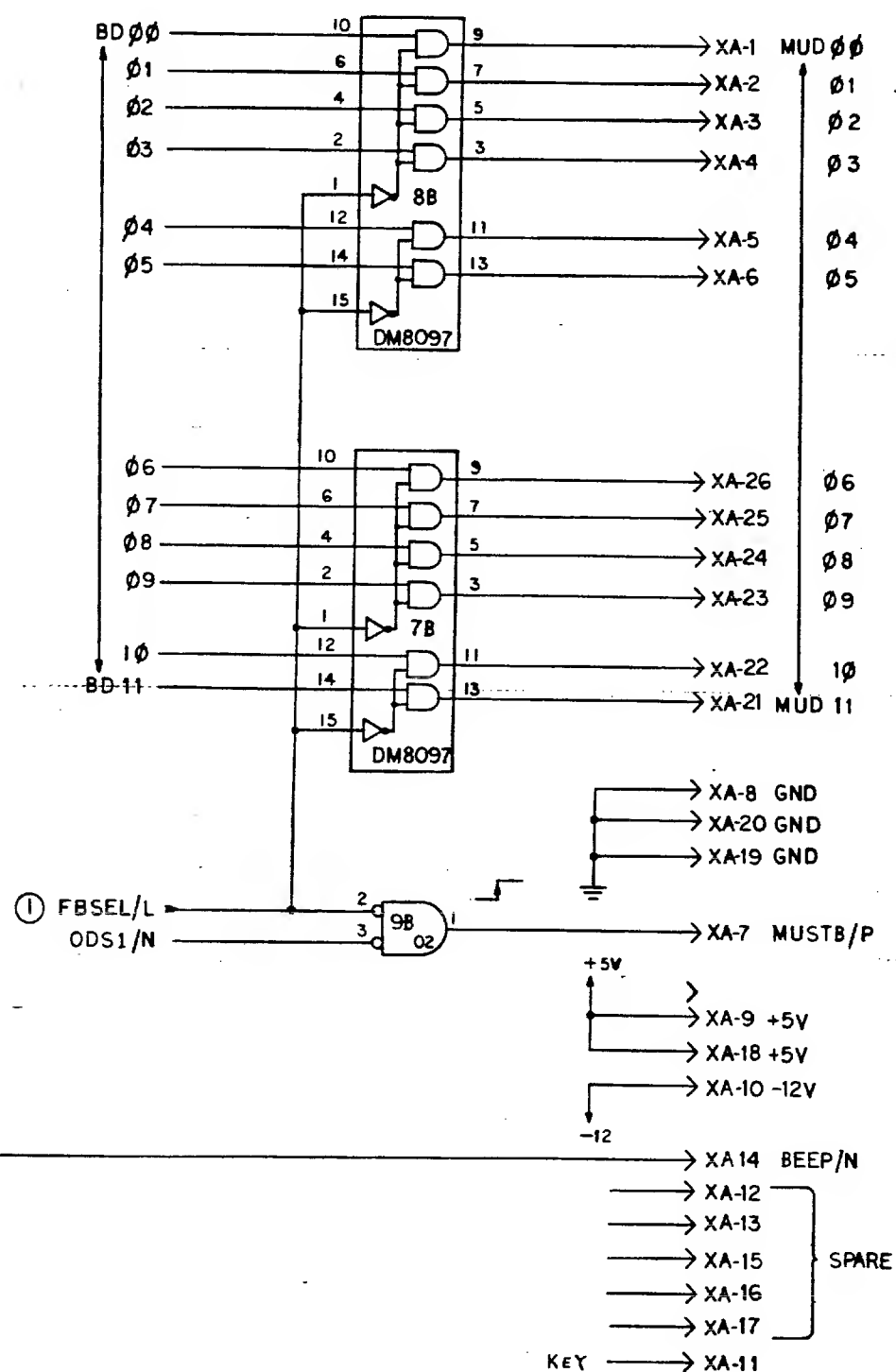
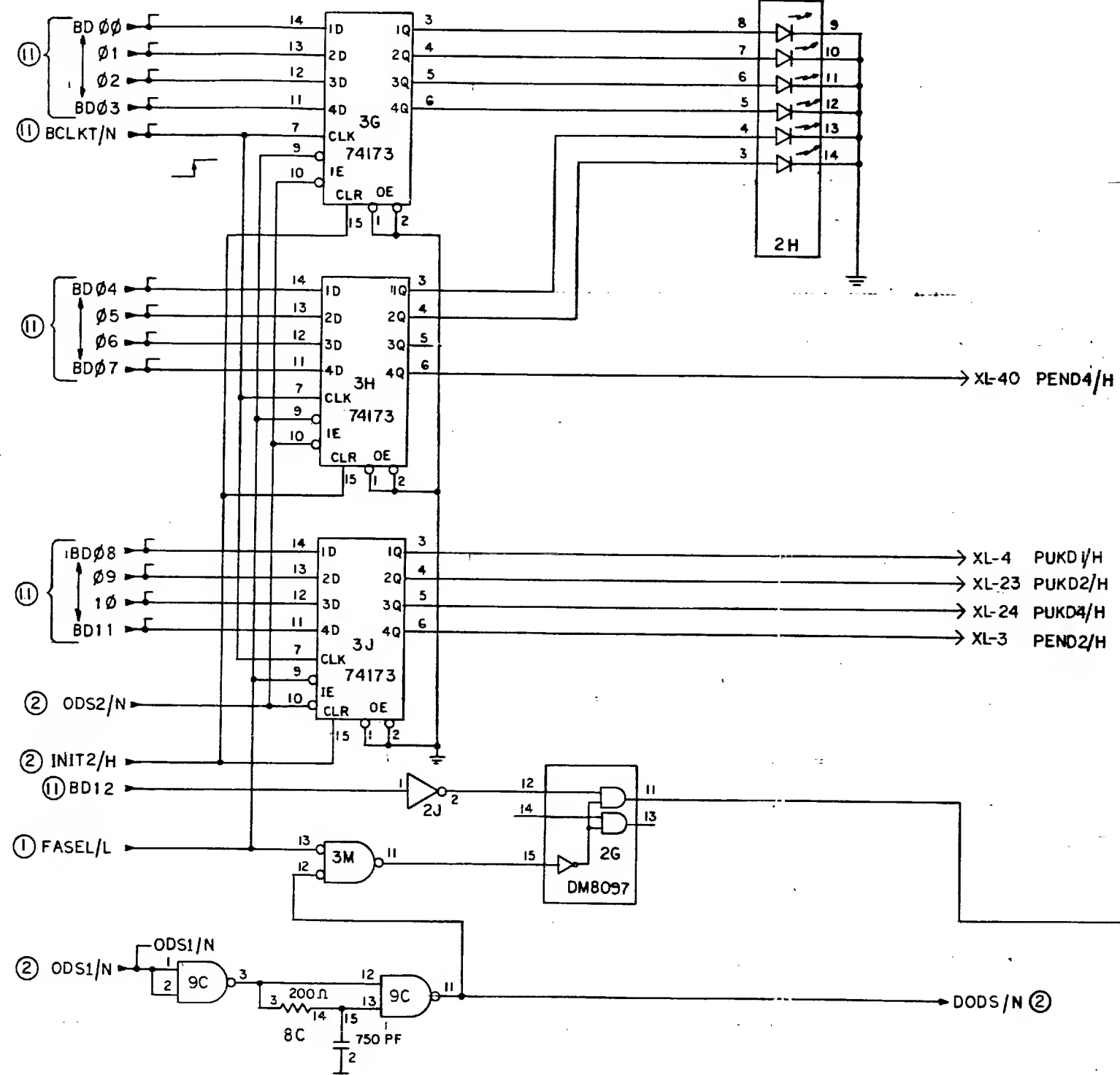
COMPUTERVISION CORP. SOUTH AVENUE BURLINGTON, MASS. 01803		REVISION DESCRIPTION DATE	
PART NUMBER NEXT ASSEMBLY QTY		SCALE 1:1	
SIGNATURE DATE		DATE	
REMOVE ALL SOLDER AND SHARP EDGES		REWORK	





WIRE PULSE & RESPONSE AND REAL TIME CLK

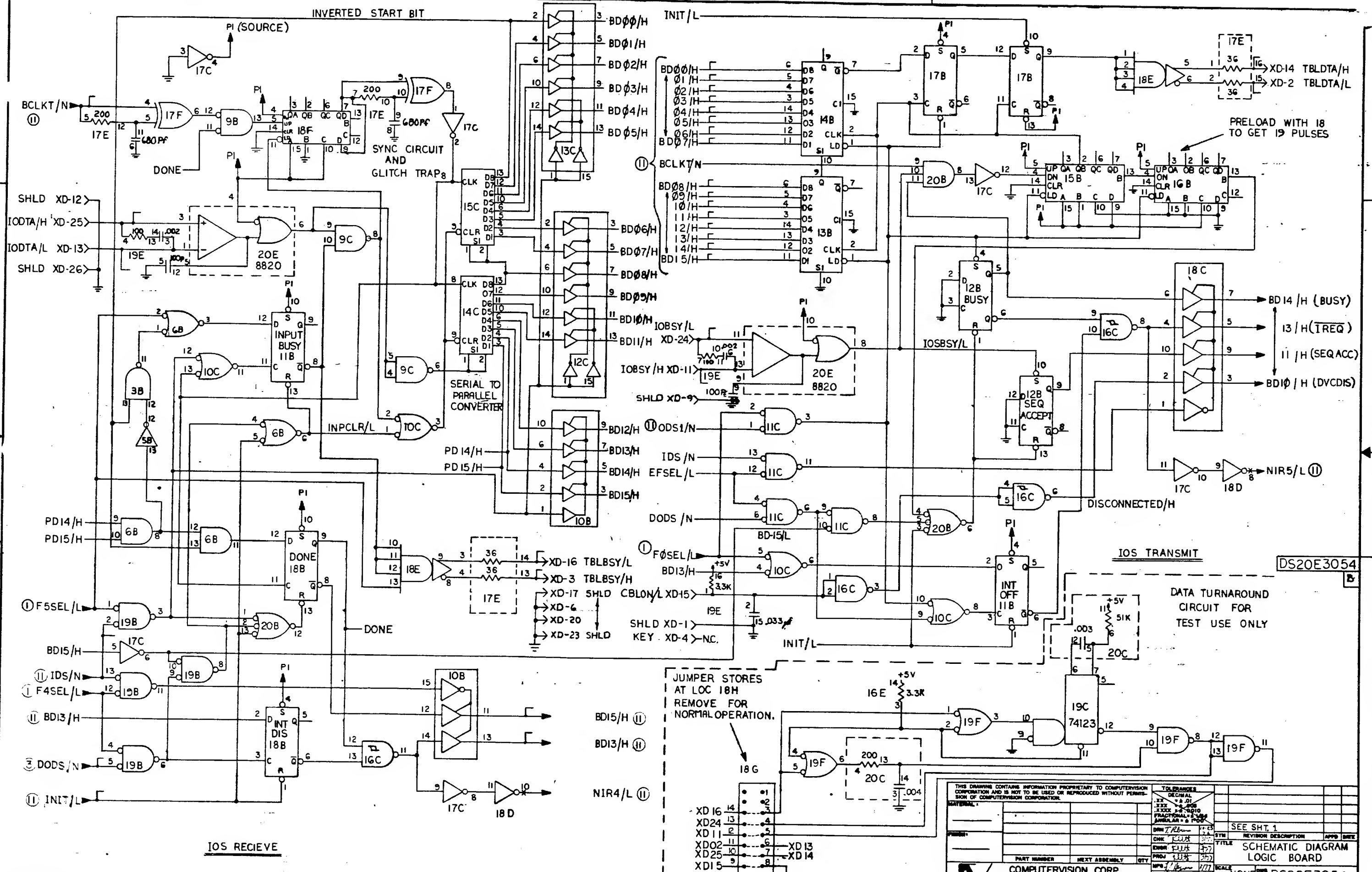
THIS DRAWING CONTAINS INFORMATION PROPRIETARY TO COMPUTERVISION CORPORATION AND IS NOT TO BE USED OR REPRODUCED WITHOUT PERMISSION OF COMPUTERVISION CORPORATION.		TOLERANCES DECIMAL .XX ± .01 .XXX ± .005 FRACTIONAL ± .0010 ANGULAR ± .004 ANGULAR ± .004		SEE SHT. 1 SYM REVISION DESCRIPTION APPD DATE TITLE SCHEMATIC DIAGRAM LOGIC BOARD	
MATERIAL:		DIM Tolerance 7/16 CHG 1.118 2-7 DIMW 1.114 3-2 DIMJ 1.114 3-2 DIMK 1.114 3-2 DIML 1.114 3-2 DIMM 1.114 3-2 DIMN 1.114 3-2 DIMO 1.114 3-2 DIMP 1.114 3-2 DIMQ 1.114 3-2 DIMR 1.114 3-2 DIMS 1.114 3-2 DIMT 1.114 3-2 DIMU 1.114 3-2 DIMV 1.114 3-2 DIMW 1.114 3-2 DIMX 1.114 3-2 DIMY 1.114 3-2 DIMZ 1.114 3-2 DIMAA 1.114 3-2 DIMAB 1.114 3-2 DIMAC 1.114 3-2 DIMAD 1.114 3-2 DIMAE 1.114 3-2 DIMAF 1.114 3-2 DIMAG 1.114 3-2 DIMAH 1.114 3-2 DIMAI 1.114 3-2 DIMAJ 1.114 3-2 DIMAK 1.114 3-2 DIMAL 1.114 3-2 DIMAM 1.114 3-2 DIMAN 1.114 3-2 DIMAO 1.114 3-2 DIMAP 1.114 3-2 DIMAQ 1.114 3-2 DIMAR 1.114 3-2 DIMAS 1.114 3-2 DIMAT 1.114 3-2 DIMAU 1.114 3-2 DIMAV 1.114 3-2 DIMAW 1.114 3-2 DIMAX 1.114 3-2 DIMAY 1.114 3-2 DIMAZ 1.114 3-2 DIMBA 1.114 3-2 DIMBB 1.114 3-2 DIMBC 1.114 3-2 DIMBD 1.114 3-2 DIMBE 1.114 3-2 DIMBF 1.114 3-2 DIMBG 1.114 3-2 DIMBH 1.114 3-2 DIMBI 1.114 3-2 DIMBJ 1.114 3-2 DIMBK 1.114 3-2 DIMBL 1.114 3-2 DIMBM 1.114 3-2 DIMBN 1.114 3-2 DIMBO 1.114 3-2 DIMBP 1.114 3-2 DIMBQ 1.114 3-2 DIMBR 1.114 3-2 DIMBS 1.114 3-2 DIMBT 1.114 3-2 DIMBU 1.114 3-2 DIMBV 1.114 3-2 DIMBW 1.114 3-2 DIMBX 1.114 3-2 DIMBY 1.114 3-2 DIMBZ 1.114 3-2 DIMCA 1.114 3-2 DIMCB 1.114 3-2 DIMCC 1.114 3-2 DIMCD 1.114 3-2 DIMCE 1.114 3-2 DIMCF 1.114 3-2 DIMCG 1.114 3-2 DIMCH 1.114 3-2 DIMCI 1.114 3-2 DIMCJ 1.114 3-2 DIMCK 1.114 3-2 DIMCL 1.114 3-2 DIMCM 1.114 3-2 DIMCN 1.114 3-2 DIMCO 1.114 3-2 DIMCP 1.114 3-2 DIMCQ 1.114 3-2 DIMCR 1.114 3-2 DIMCS 1.114 3-2 DIMCT 1.114 3-2 DIMCU 1.114 3-2 DIMCV 1.114 3-2 DIMCW 1.114 3-2 DIMCX 1.114 3-2 DIMCY 1.114 3-2 DIMCZ 1.114 3-2 DIMDA 1.114 3-2 DIMDB 1.114 3-2 DIMDC 1.114 3-2 DIMDD 1.114 3-2 DIMDE 1.114 3-2 DIMDF 1.114 3-2 DIMDG 1.114 3-2 DIMDH 1.114 3-2 DIMDI 1.114 3-2 DIMDJ 1.114 3-2 DIMDK 1.114 3-2 DIMDL 1.114 3-2 DIMDM 1.114 3-2 DIMDN 1.114 3-2 DIMDO 1.114 3-2 DIMDP 1.114 3-2 DIMDQ 1.114 3-2 DIMDR 1.114 3-2 DIMDS 1.114 3-2 DIMDT 1.114 3-2 DIMDU 1.114 3-2 DIMDV 1.114 3-2 DIMDW 1.114 3-2 DIMDX 1.114 3-2 DIMDY 1.114 3-2 DIMDZ 1.114 3-2 DIMEA 1.114 3-2 DIMEB 1.114 3-2 DIMEC 1.114 3-2 DIMED 1.114 3-2 DIMEE 1.114 3-2 DIMEF 1.114 3-2 DIMEG 1.114 3-2 DIMEH 1.114 3-2 DIMEI 1.114 3-2 DIMEJ 1.114 3-2 DIMEK 1.114 3-2 DIMEL 1.114 3-2 DIMEM 1.114 3-2 DIMEN 1.114 3-2 DIMEO 1.114 3-2 DIMEP 1.114 3-2 DIMEQ 1.114 3-2 DIMER 1.114 3-2 DIMES 1.114 3-2 DIMET 1.114 3-2 DIMEU 1.114 3-2 DIMEV 1.114 3-2 DIMEW 1.114 3-2 DIMEX 1.114 3-2 DIMEY 1.114 3-2 DIMEZ 1.114 3-2 DIMFA 1.114 3-2 DIMFB 1.114 3-2 DIMFC 1.114 3-2 DIMFD 1.114 3-2 DIMFE 1.114 3-2 DIMFF 1.114 3-2 DIMFG 1.114 3-2 DIMFH 1.114 3-2 DIMFI 1.114 3-2 DIMFJ 1.114 3-2 DIMFK 1.114 3-2 DIMFL 1.114 3-2 DIMFM 1.114 3-2 DIMFN 1.114 3-2 DIMFO 1.114 3-2 DIMFP 1.114 3-2 DIMFQ 1.114 3-2 DIMFR 1.114 3-2 DIMFS 1.114 3-2 DIMFT 1.114 3-2 DIMFU 1.114 3-2 DIMFV 1.114 3-2 DIMFW 1.114 3-2 DIMFX 1.114 3-2 DIMFY 1.114 3-2 DIMFZ 1.114 3-2 DIMGA 1.114 3-2 DIMGB 1.114 3-2 DIMGC 1.114 3-2 DIMGD 1.114 3-2 DIMGE 1.114 3-2 DIMGF 1.114 3-2 DIMGG 1.114 3-2 DIMGH 1.114 3-2 DIMGI 1.114 3-2 DIMGJ 1.114 3-2 DIMGK 1.114 3-2 DIMGL 1.114 3-2 DIMGM 1.114 3-2 DIMGN 1.114 3-2 DIMGO 1.114 3-2 DIMGP 1.114 3-2 DIMGQ 1.114 3-2 DIMGR 1.114 3-2 DIMGS 1.114 3-2 DIMGT 1.114 3-2 DIMGU 1.114 3-2 DIMGV 1.114 3-2 DIMGW 1.114 3-2 DIMGX 1.114 3-2 DIMGY 1.114 3-2 DIMGZ 1.114 3-2 DIMHA 1.114 3-2 DIMHB 1.114 3-2 DIMHC 1.114 3-2 DIMHD 1.114 3-2 DIMHE 1.114 3-2 DIMHF 1.114 3-2 DIMHG 1.114 3-2 DIMHH 1.114 3-2 DIMHI 1.114 3-2 DIMHJ 1.114 3-2 DIMHK 1.114 3-2 DIMHL 1.114 3-2 DIMHM 1.114 3-2 DIMHN 1.114 3-2 DIMHO 1.114 3-2 DIMHP 1.114 3-2 DIMHQ 1.114 3-2 DIMHR 1.114 3-2 DIMHS 1.114 3-2 DIMHT 1.114 3-2 DIMHU 1.114 3-2 DIMHV 1.114 3-2 DIMHW 1.114 3-2 DIMHX 1.114 3-2 DIMHY 1.114 3-2 DIMHZ 1.114 3-2 DIMIA 1.114 3-2 DIMIB 1.114 3-2 DIMIC 1.114 3-2 DIMID 1.114 3-2 DIMIE 1.114 3-2 DIMIF 1.114 3-2 DIMIG 1.114 3-2 DIMIH 1.114 3-2 DIMII 1.114 3-2 DIMIJ 1.114 3-2 DIMIK 1.114 3-2 DIMIL 1.114 3-2 DIMIM 1.114 3-			

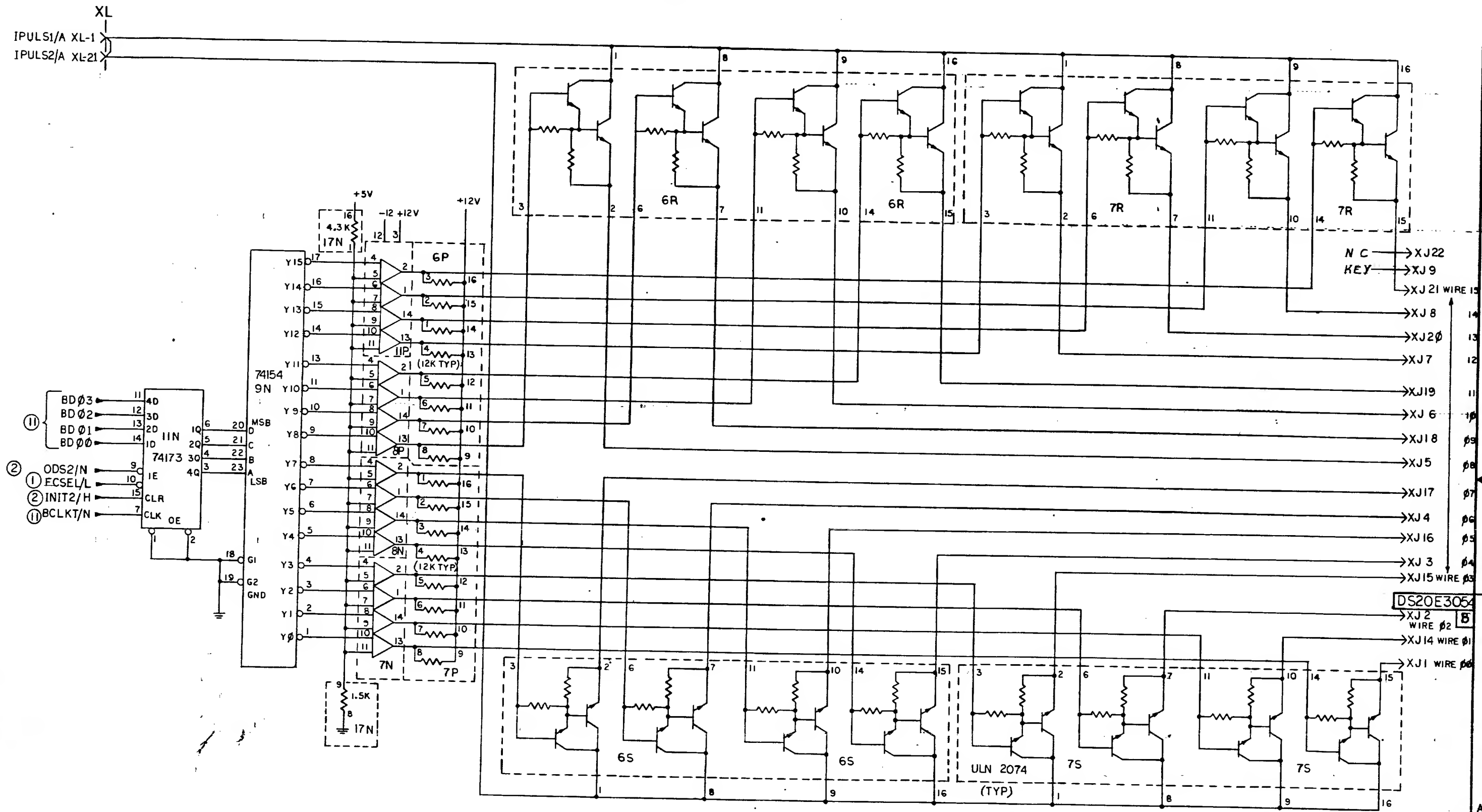


DS20E3054
5

INDICATORS & MESSAGE UNIT INTERFACE

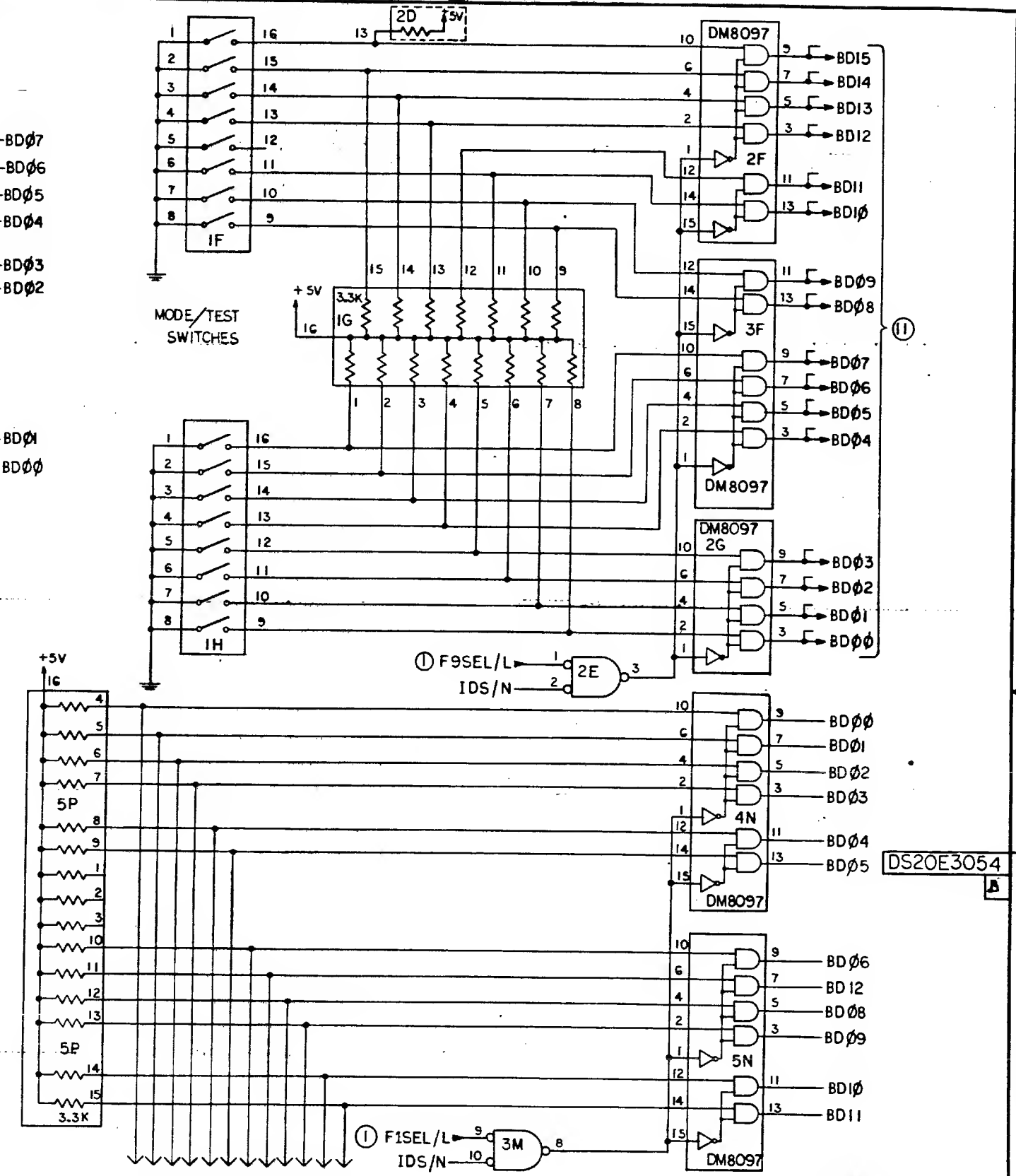
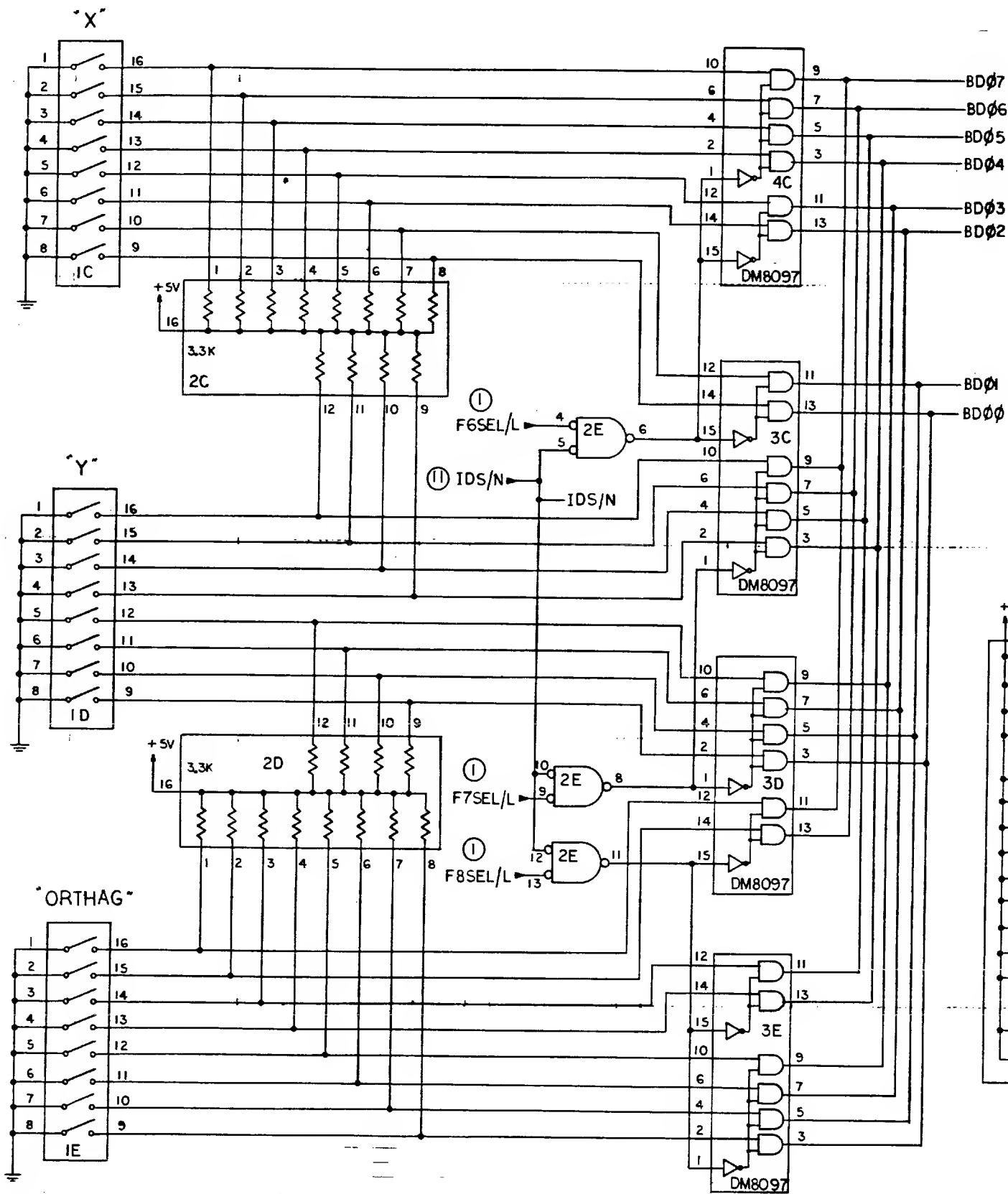
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PART NUMBER: SEE SHT. 1 NEXT ASSEMBLY: SEE SHT. 1 QTY: 1				TITLE: SCHEMATIC DIAGRAM LOGIC BOARD NO. DS20E3054 SHEET 3 OF 14	
COMPUTERVISION CORP. SOUTH AVENUE BURLINGTON, MASS. 01803				REMOVE ALL BURNING AND SHARP EDGES DATE: 10/1/77 BY: J. J. J.	





WIRE SELECT (PART-A)

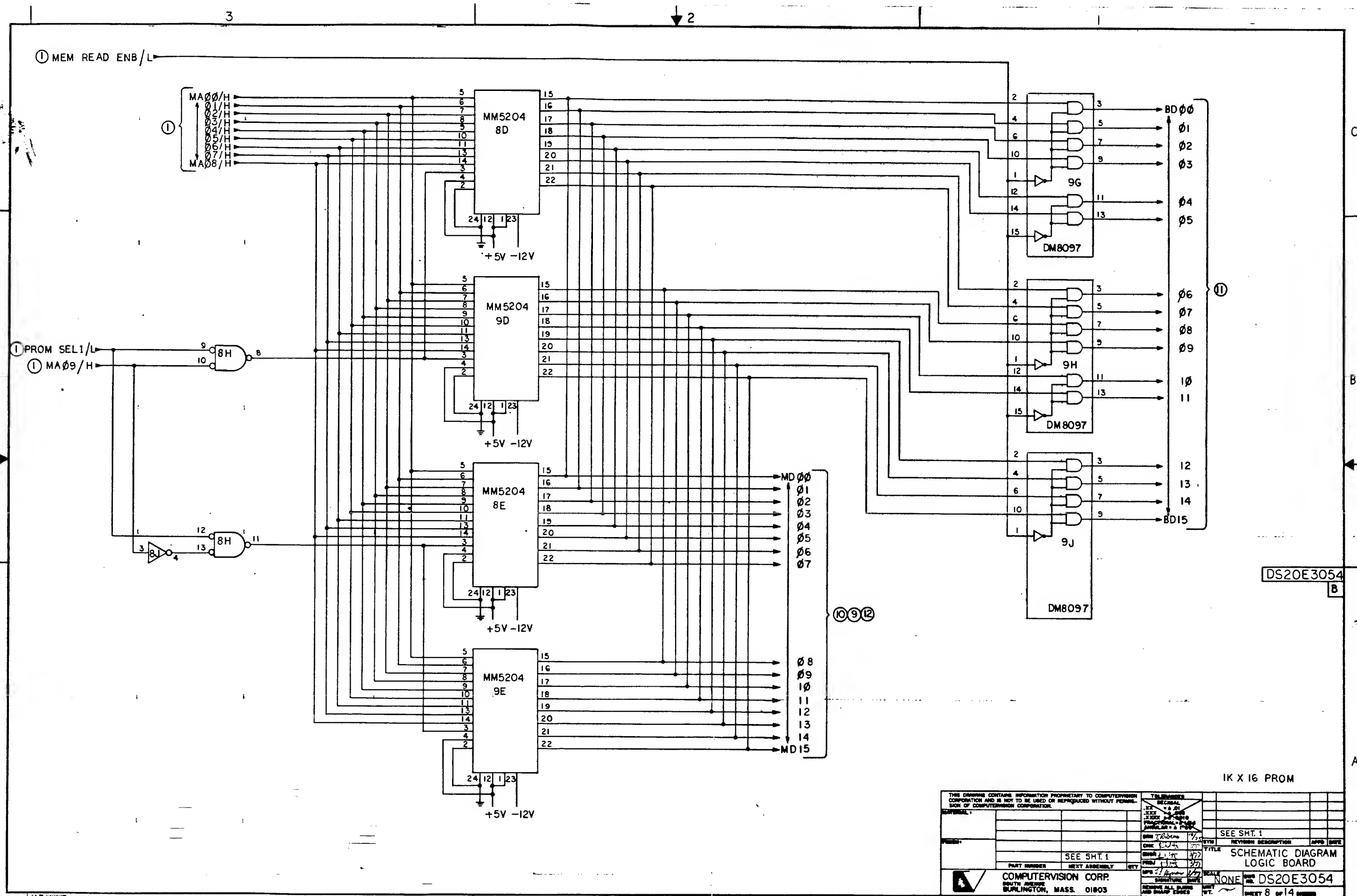
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MATERIAL				SEE SHT.1		TITLE		SCHEMATIC DIAGRAM LOGIC BOARD	
PART NUMBER				NEXT ASSEMBLY		QTY		COMPUTERVISION CORP. SOUTH AVENUE BURLINGTON, MASS. 01803	
DESIGNED BY				CHKD BY		ENGR BY		PROJ BY	
DATE				DATE		DATE		DATE	
REWORK ALL DIMS AND SHARP EDGES				NONE		NONE		NONE	
PART NO.				DS20E3054		SHEET		5 of 14	



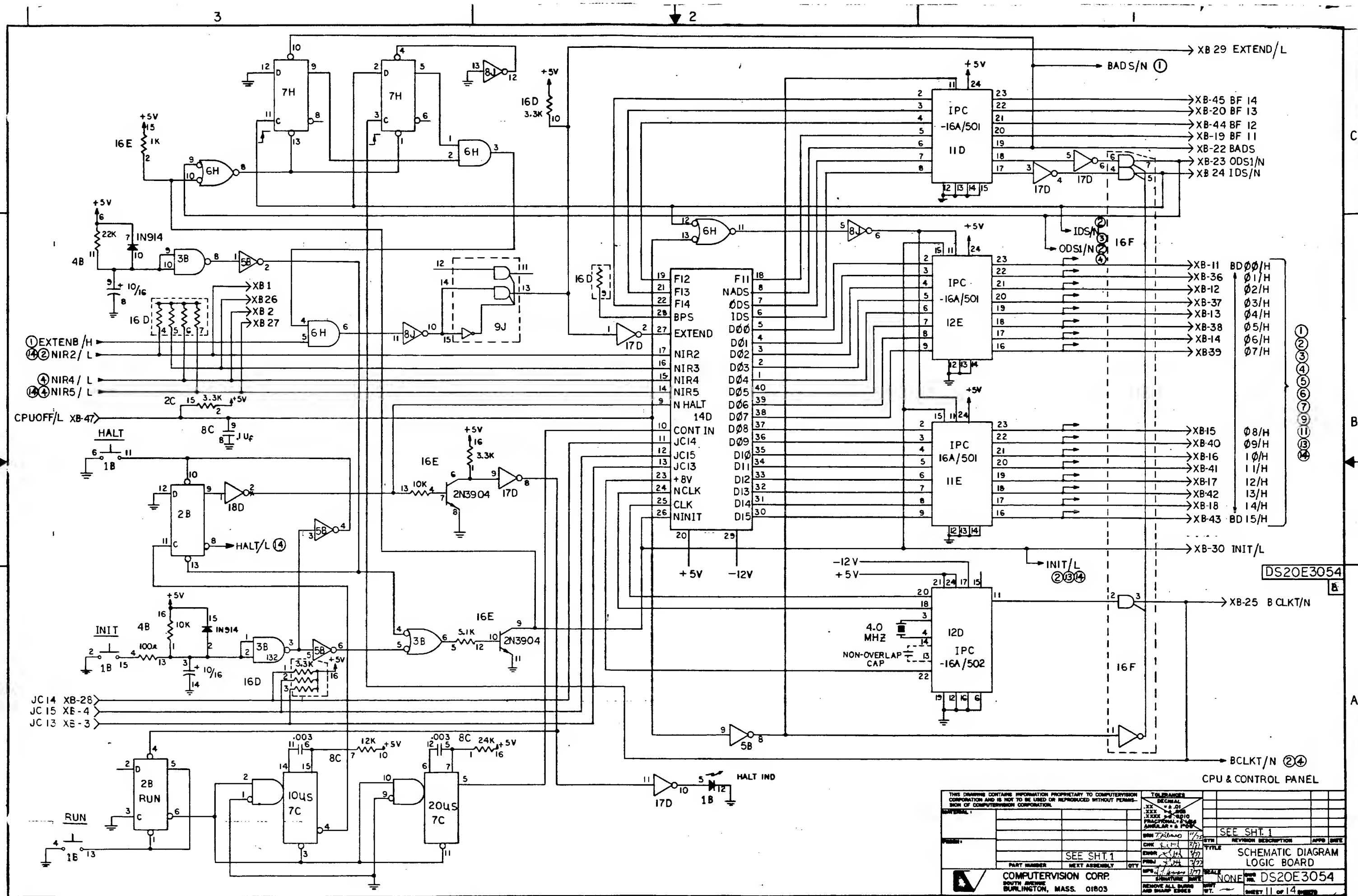
SWITCH & BUTTONS INPUTS

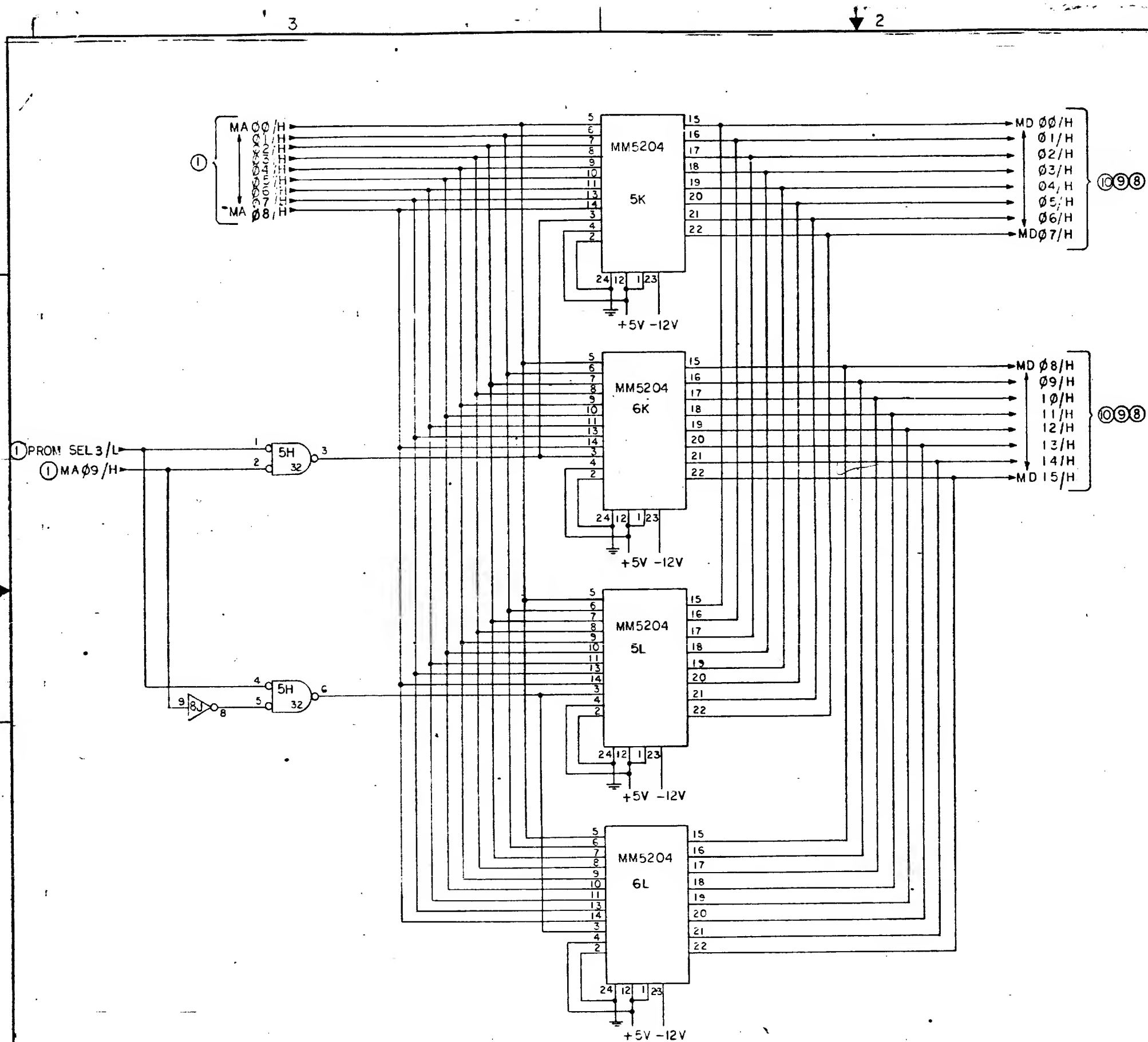
XL-5	XL-6	XL-7	XL-25	XL-26	XL-8	XL-27	XL-33	XJ-13	XJ-25	XJ-12	XJ-24
PUK51/L	PUK52/L	PUK53/L	PUK54/L	PUK55/L	PENS1/L	PENS2/L	PENS3/L	TABS1/L	TABS2/L	TABS3/L	TABS4/L

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DATE	CHK	REV	SYN
10/1/78	10/1/78	10/1/78	10/1/78
SEE SHT. 1		TITLE	
PART NUMBER		NEXT ASSEMBLY	
COMPUTERVISION CORP.		SCALE	
SOUTH AVENUE		NONE	
BURLINGTON, MASS. 01803		UNIT	
REMOVE ALL BURNS AND SHARP EDGES		SHEET 6 OF 14 SHEETS	



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REVISION				REV. 1				REV. 2			
PART NUMBER				NEXT ASSEMBLY				QTY			
SEE SHT. 1				SEE SHT. 1				SEE SHT. 1			
COMPUTERVISION CORP. SOUTH AVENUE BURLINGTON, MASS. 01803				SCALE NONE				DRAWN DS20E3054			
SHEET 8 OF 14				TITLE SCHEMATIC DIAGRAM LOGIC BOARD				APPD DATE			





DS20E3054

B

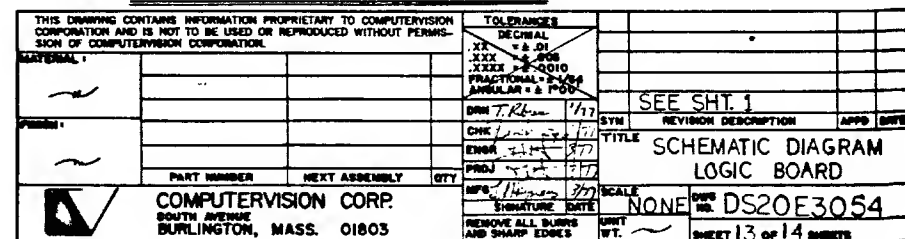
1K X 16 PROM

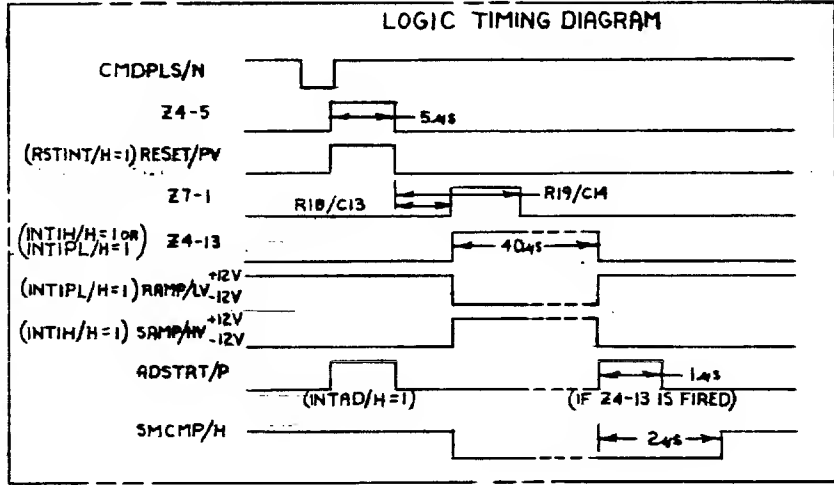
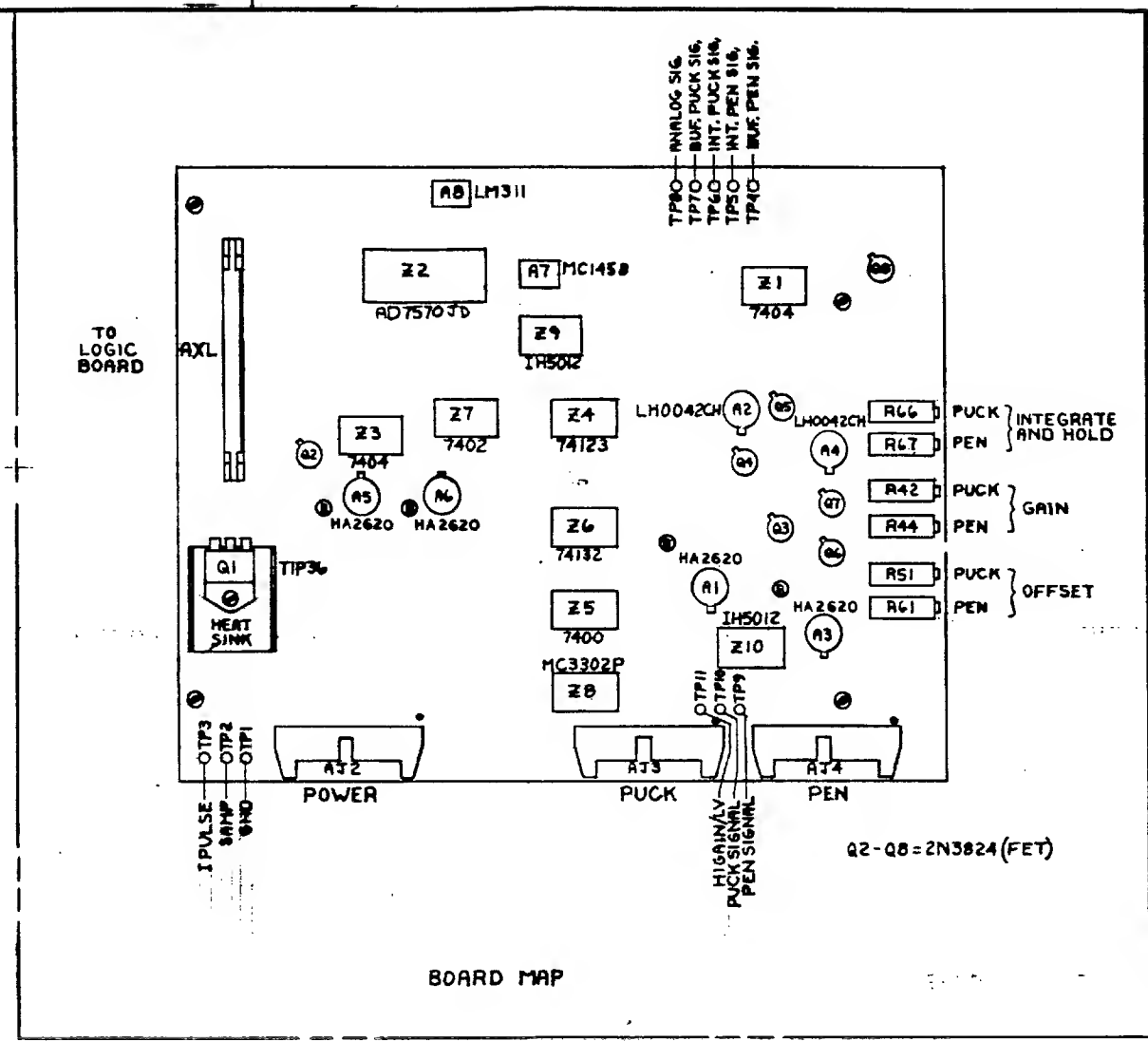
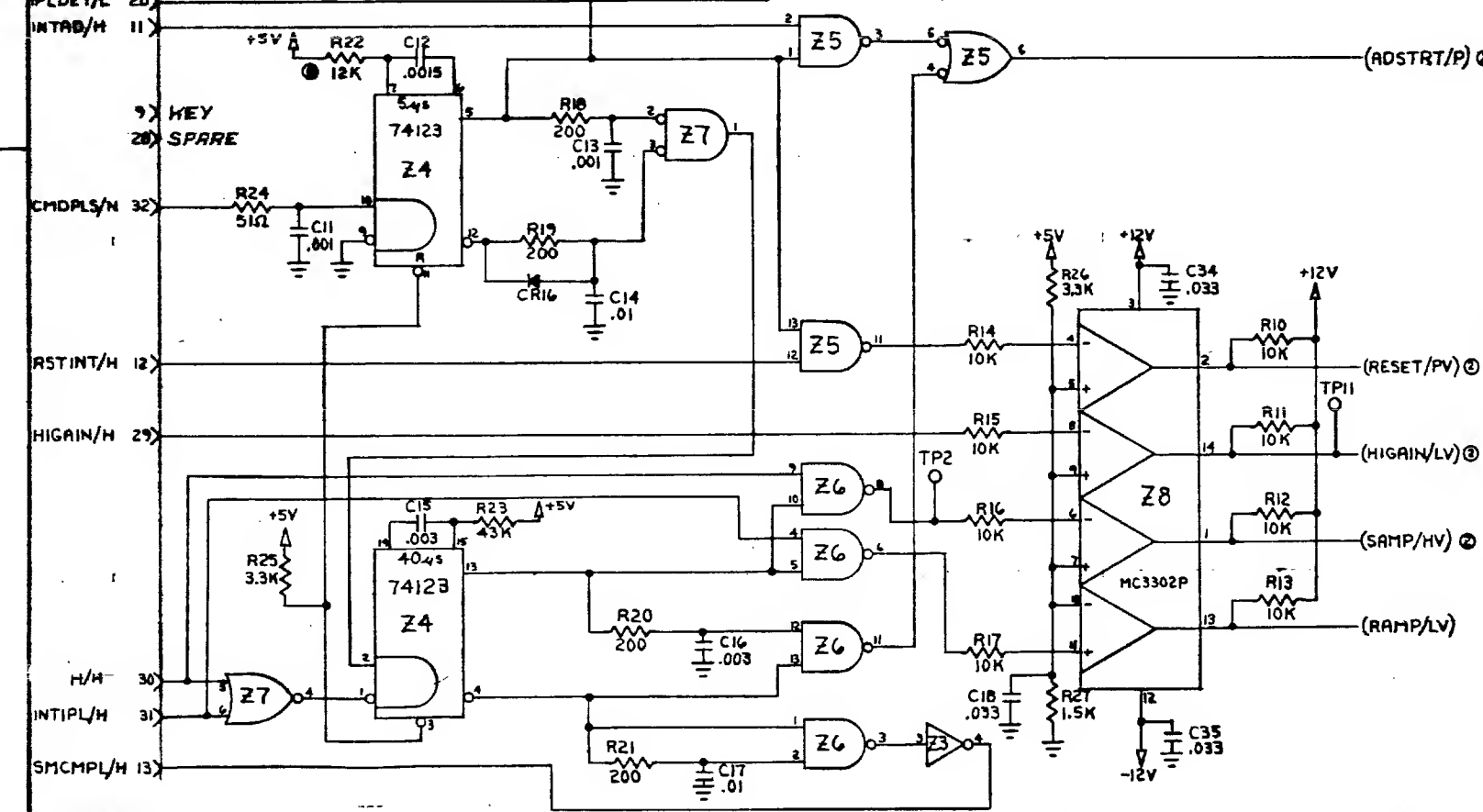
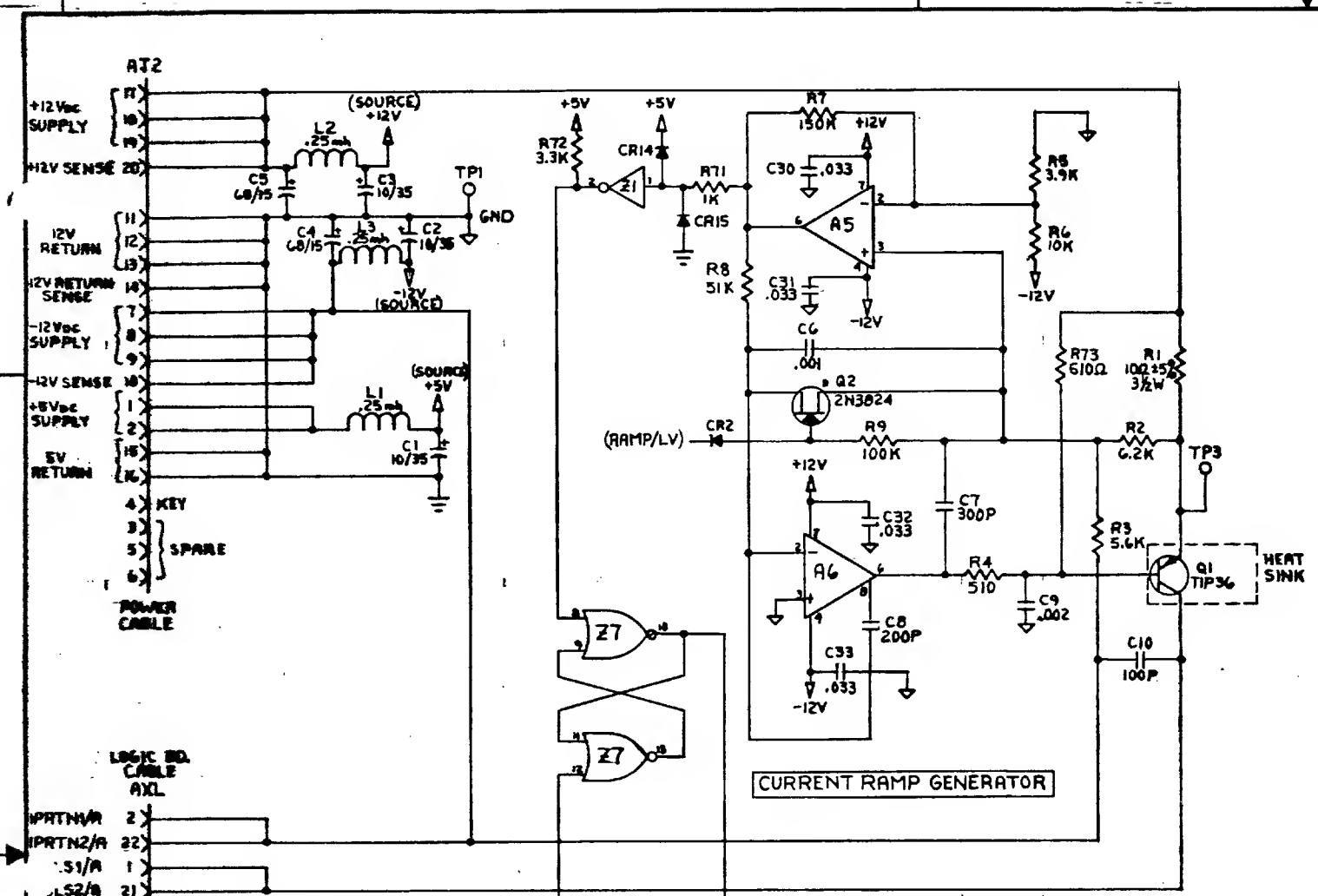
THIS DRAWING CONTAINS INFORMATION PROPRIETARY TO COMPUTERVISION CORPORATION AND IS NOT TO BE USED OR REPRODUCED WITHOUT PERMISSION OF COMPUTERVISION CORPORATION.				TOLERANCES DECIMAL XX ± .01 XXX ± .005 XXXX ± .0010 FRACTIONAL ANGULAR ± .008							
MATERIAL				DATE				SYN			
FINISH				ENGR				TITLE			
PART NUMBER				NEXT ASSEMBLY				QTY			
COMPUTERVISION CORP. SOUTH AVENUE BURLINGTON, MASS. 01803				SCALE NONE				DWS NO. DS20E3054			
REMOVE ALL BURRS AND SHARP EDGES				UNIT				SHEET 12 OF 14 SHEETS			

The diagram shows a 14N04 dual inverter with the following connections and components:

- Power Supply:**
 - +12V:** Connected to pin 9 (V_{DD}).
 - +5V:** Connected to pin 12 (V_{CC}).
 - Ground:** Connected to pin 11.
- Crystal Oscillator:**
 - A 5.0688 MHz XTAL crystal is connected between pins 1 (X1) and 16 (X2).
 - A 14M resistor is connected between pins 1 and 16.
- Clock Inputs:**
 - COMCLK:** Connected to pin 17 (F0).
 - UCLK:** Connected to pin 3 (F0).
- Package Layout:**
 - The device is shown in a 20-pin package with pins 1-4 on the left and 5-8 on the right.
 - Internal labels A, B, C, D are shown for pins 16, 15, 14, 13 and 4, 5, 6, 7.
 - Internal labels A, B, C, D are shown for pins 12, 11, 10, 9.

A	B	C	D	BAUD RATE	16 X CLOCK FREQ. KHZ
•	•	•	•	50	0.8
	•	•	•	75	1.2
•		•	•	110	1.76
		•	•	134.5	2.1523
•	•		•	150	2.4
	•		•	300	4.8
•			•	600	9.6
			•	1200	19.2
•	•	•		1800	28.8
	•	•		2000	32.081
•		•		2400	38.4
		•		3600	57.6
•	•			4800	76.8
	•			7200	115.2
•				9600	153.6
				19,200	316.8

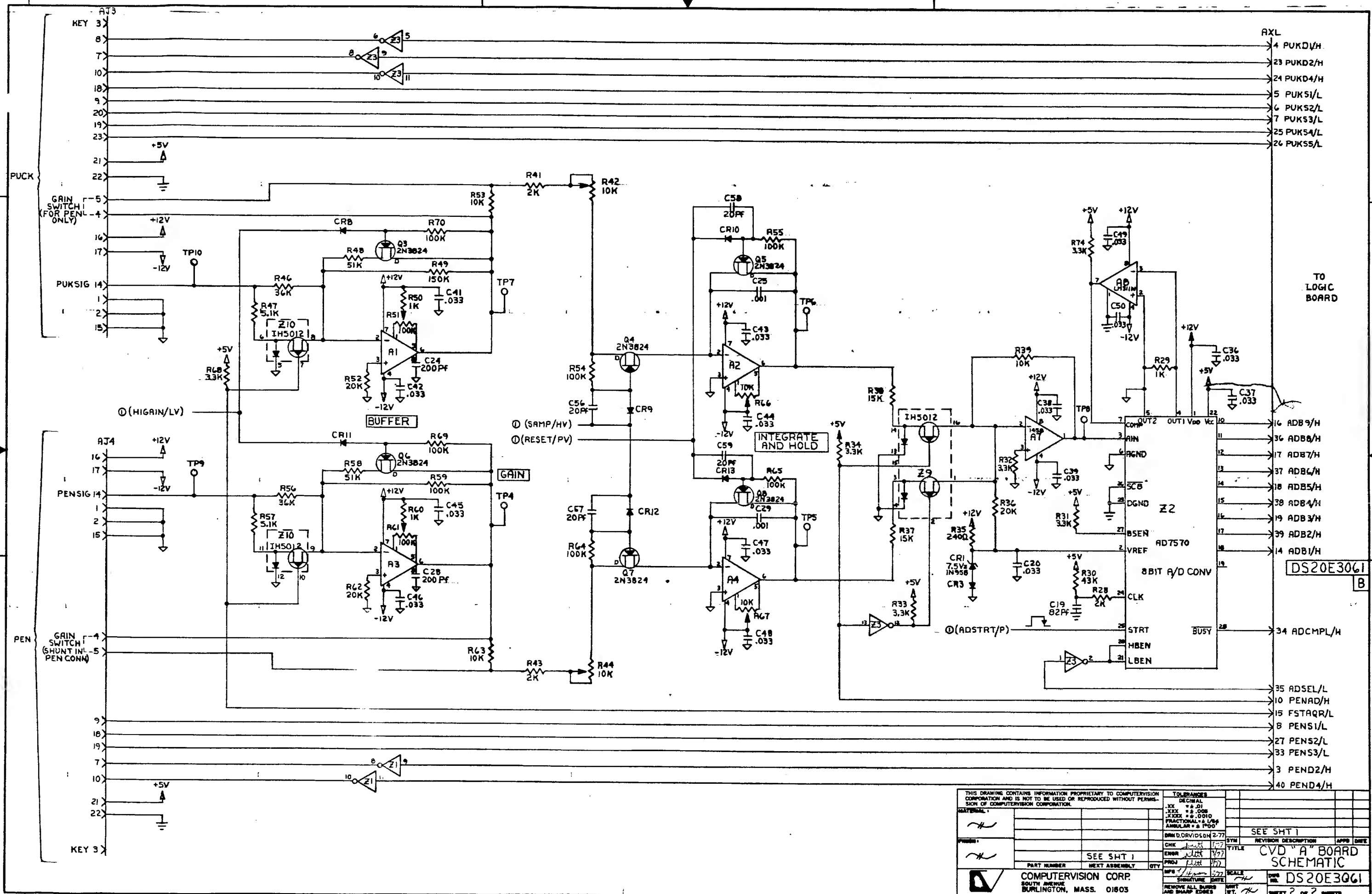




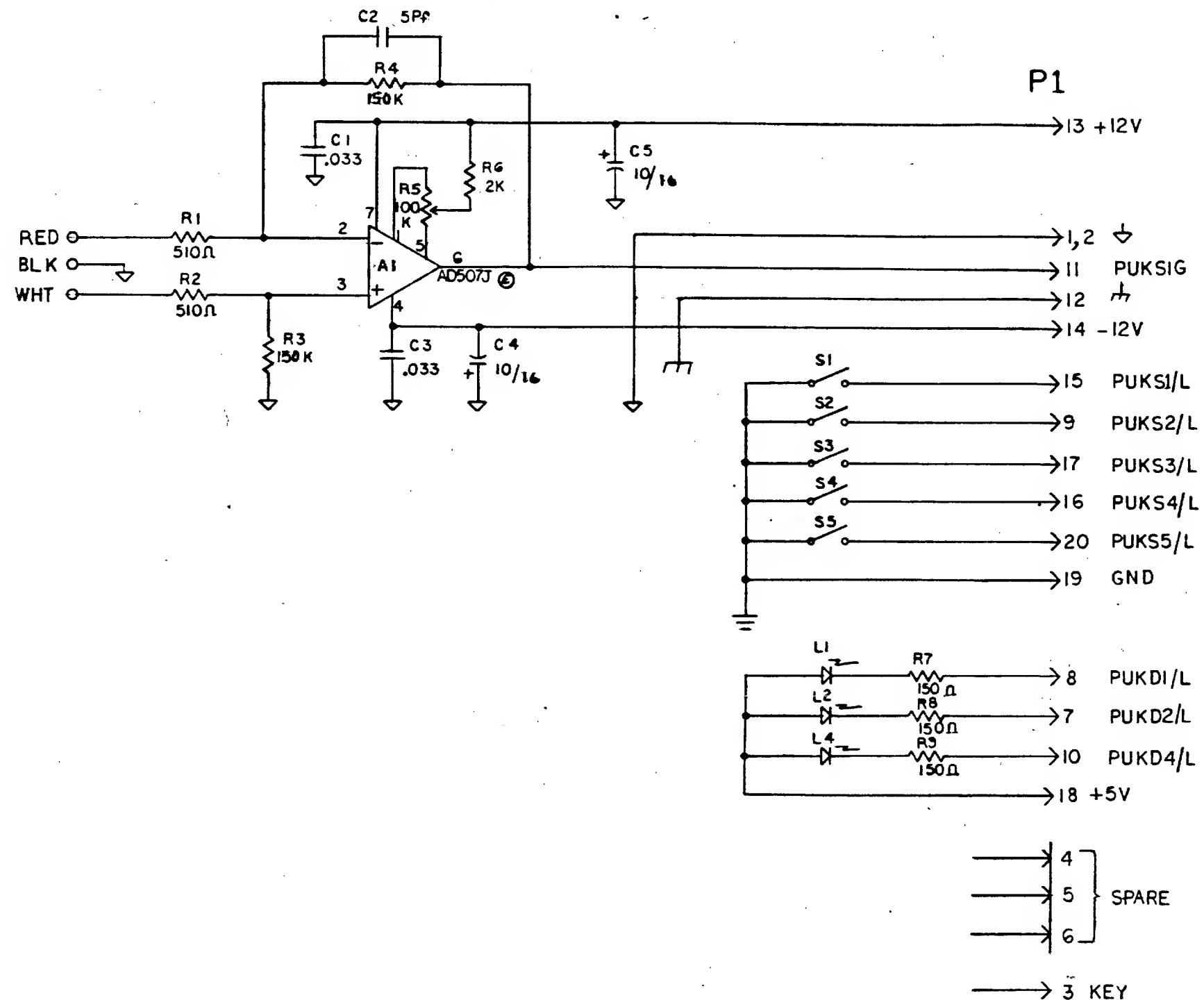
NOTES:

- C51 thru C55 ARE .033 DECOUPLING CAPACITORS FOR THE 5 LOGIC I.C.'S (NOT SHOWN ON SCHEMATIC).
- ALL 2N3824 FET'S - CONNECT CASE TO DRAIN.
- HIGHEST COMPONENT NUMBERS: C59, R74, CR16, Q8.

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B ECO # 2736		REV A 3-77	
A REL ECO 2270		REV B 3-77	
L20X3007		CHK 111	
DA20E3060		CHK 111	
PART NUMBER		NEXT ASSEMBLY	
COMPUTERVISION CORP.		SOUTH AVENUE BURLINGTON, MASS. 01803	
REMOVE ALL BURRS AND SHARP EDGES		DATE	
SHEET 1 OF 2		REV A = REV 2	



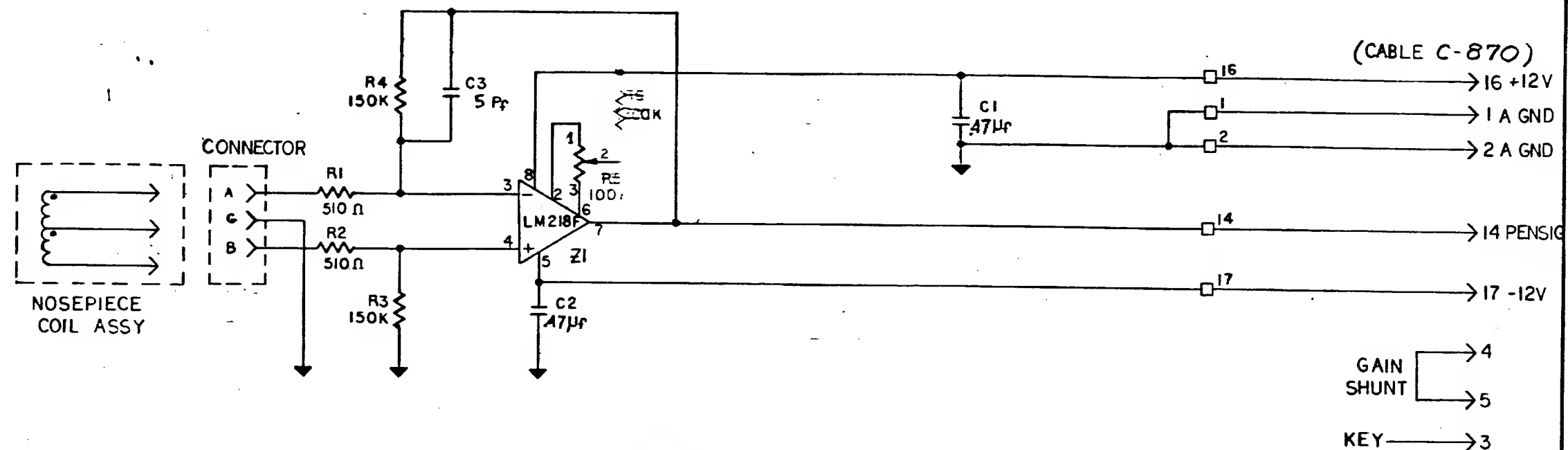
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TOLERANCES		DECIMAL	
.XX		±.01	
.XXX		±.005	
.XXXX		±.0010	
FRACTIONAL		±.0005	
ANGULAR		±.1°	
DRAWN		DAVIDSON 2-77	
CHK		JUL 77	
ENGR		JUL 77	
PROJ		JUL 77	
MFG		JUL 77	
SIGNATURE		DATE	
REMOVE ALL BURRS AND SHARP EDGES		W.T.	
PART NUMBER		SEE SHT 1	
NEXT ASSEMBLY		QTY	
COMPUTERVISION CORP.		BURLINGTON, MASS. 01803	
TITLE		CVD "A" BOARD SCHEMATIC	
DWN		DS20E3061	
SHEET		2 OF 2 SHEETS	



CS20E2068

E

THIS DRAWING CONTAINS INFORMATION PROPRIETARY TO COMPUTERVISION CORPORATION AND IS NOT TO BE USED OR REPRODUCED WITHOUT PERMISSON OF COMPUTERVISION CORPORATION.				TOLERANCES DECIMAL = ± .01 XXX = ± .005 XXXX = ± .0010 FRACTIONAL = 1/64 ANGULAR = ± 1°00'		D ECO #2270 C ECO #2156 B Release ECO 2065 E PER ECO #2837	
MATERIAL:				DRN T. R. Bero 1/16		SYM	
FURNISH:				CHK		REVISION DESCRIPTION	
PART NUMBER				NEXT ASSEMBLY		TITLE	
CA20E2065				RE		SCHEMATIC DIAGRAM PUCK	
COMPUTERVISION CORP. 201 BURLINGTON RD. (RT. 62) BEDFORD, MASS. 01730				SIGNATURE		SCALE NONE	
REMOVE ALL BUMPS AND SHARP EDGES				DATE		DWS NO. CS20E2068	
				UNIT WT.		SHEET 1 OF 1 SHEETS	

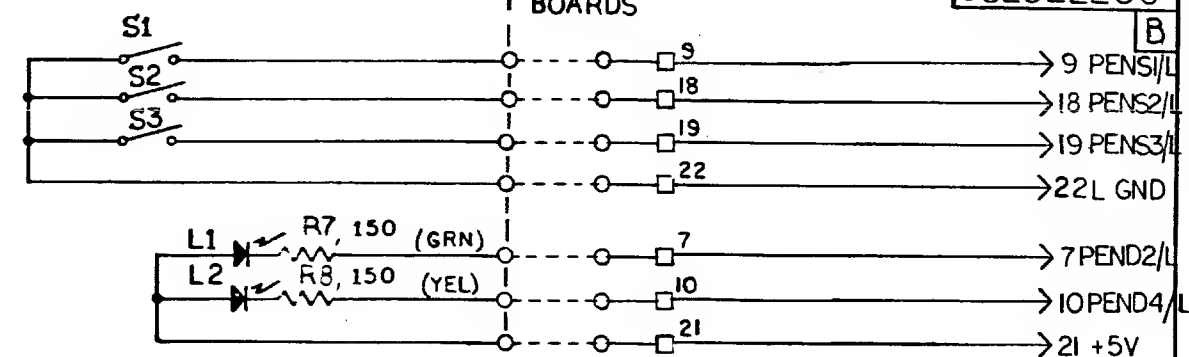


AMPLIFIER BOARD

SWITCH BOARD

STANDOFF
JUMPERS
BETWEEN
BOARDS

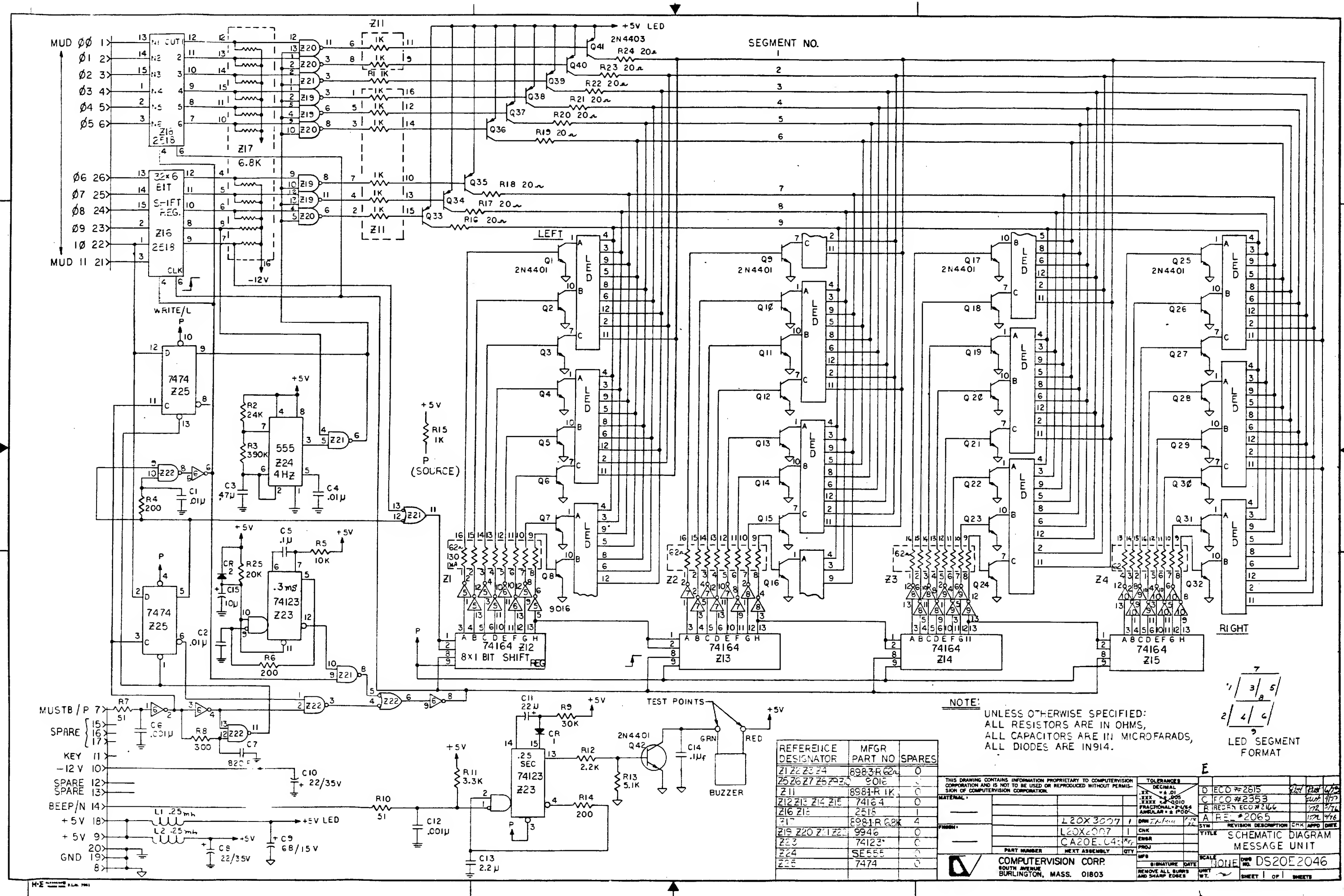
CS20E2236

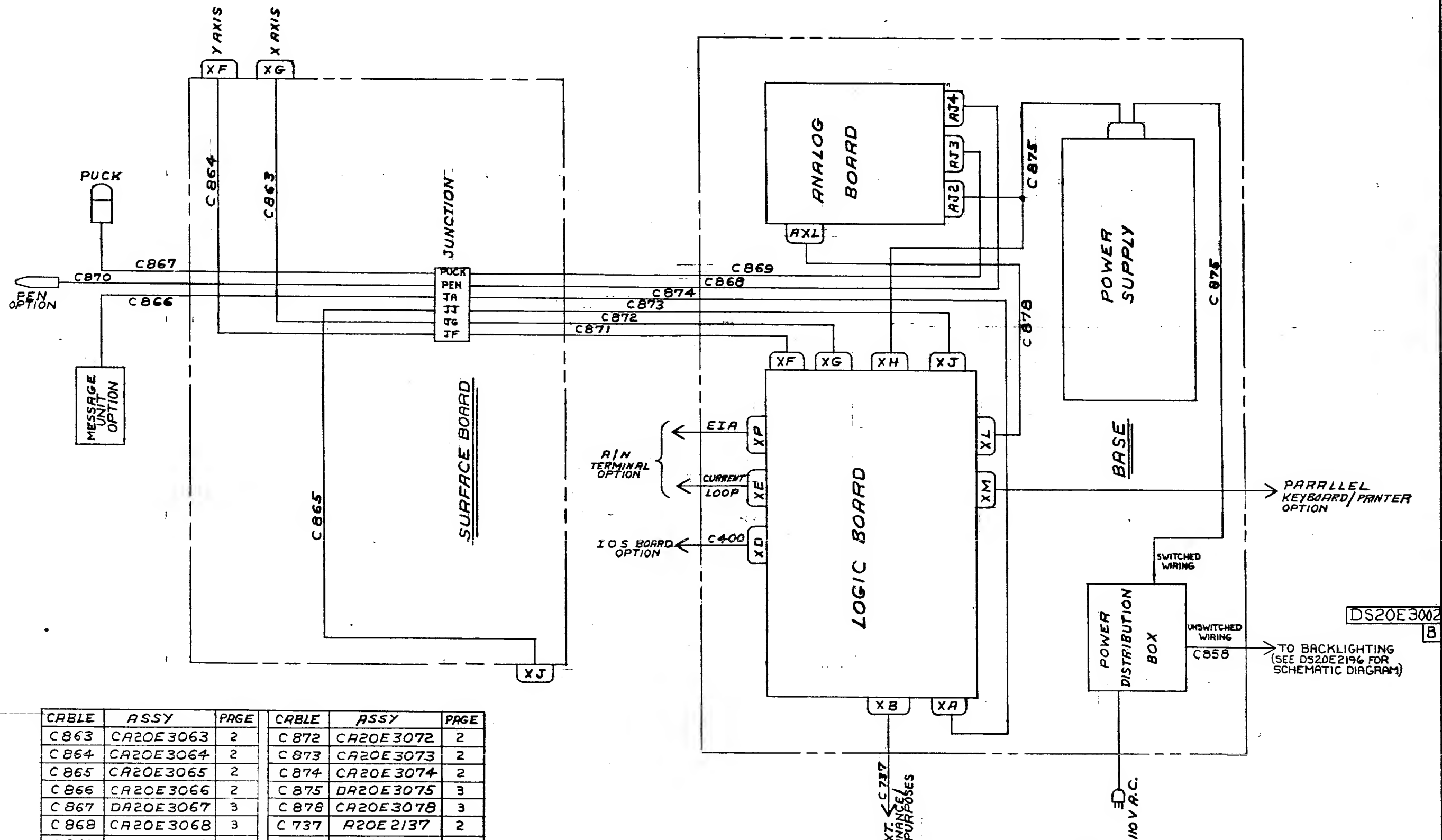


NOTES:

1, PEND4 & PENS3 NOT USED ON "A" VERSION CVD.

THIS DRAWING CONTAINS INFORMATION PROPRIETARY TO COMPUTERVISION CORPORATION AND IS NOT TO BE USED OR REPRODUCED WITHOUT PERMISS				TOLERANCES			
				DECIMAL			
				.XX ± .01			
				.XXX ± .005			
				.XXXX ± .0010			
				FRACTIONAL ± 1/64			
				ANGULAR ± 1°00'			
MATERIAL:				A20R3004		B ECO #2425	
PUSH:				L20X3007		A REL ECO 2340	
				CA20E2235		SYM REVISION DESCRIPTION APPD DATE	
				CA20E2240		TITLE SCHEMATIC DIAGRAM CVD PEN	
PART NUMBER				NEXT ASSEMBLY		QTY	
COMPUTERVISION CORP.				201 BURLINGTON RD. (RT. 62)		SCALE NONE	
BEDFORD, MASS. 01730						DWG NO. CS20E2236	
				SIGNATURE DATE		UNIT WT.	
				REMOVE ALL BURRS AND SHARP EDGES		SHEET 1 OF 1 SHEETS	

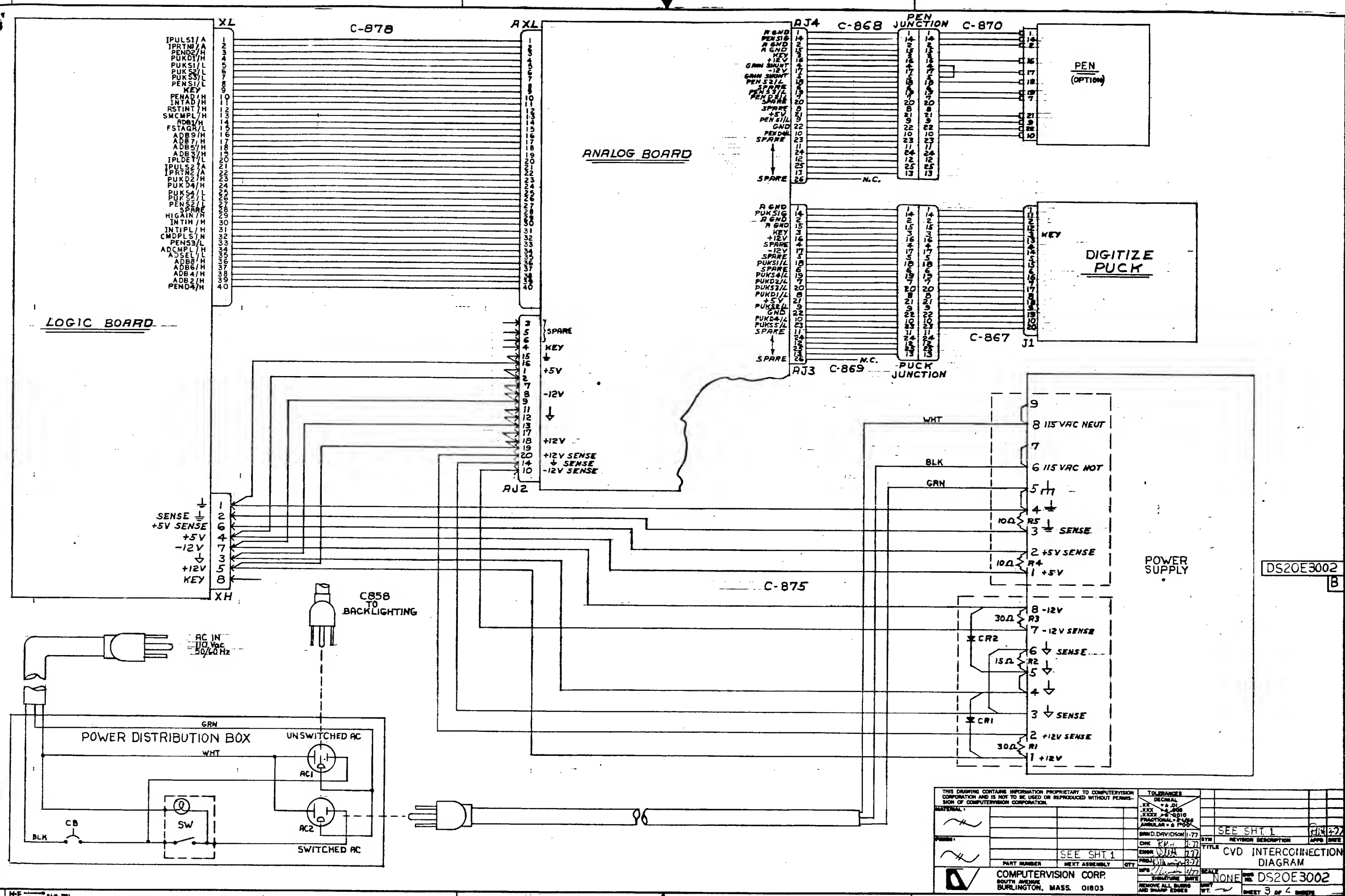


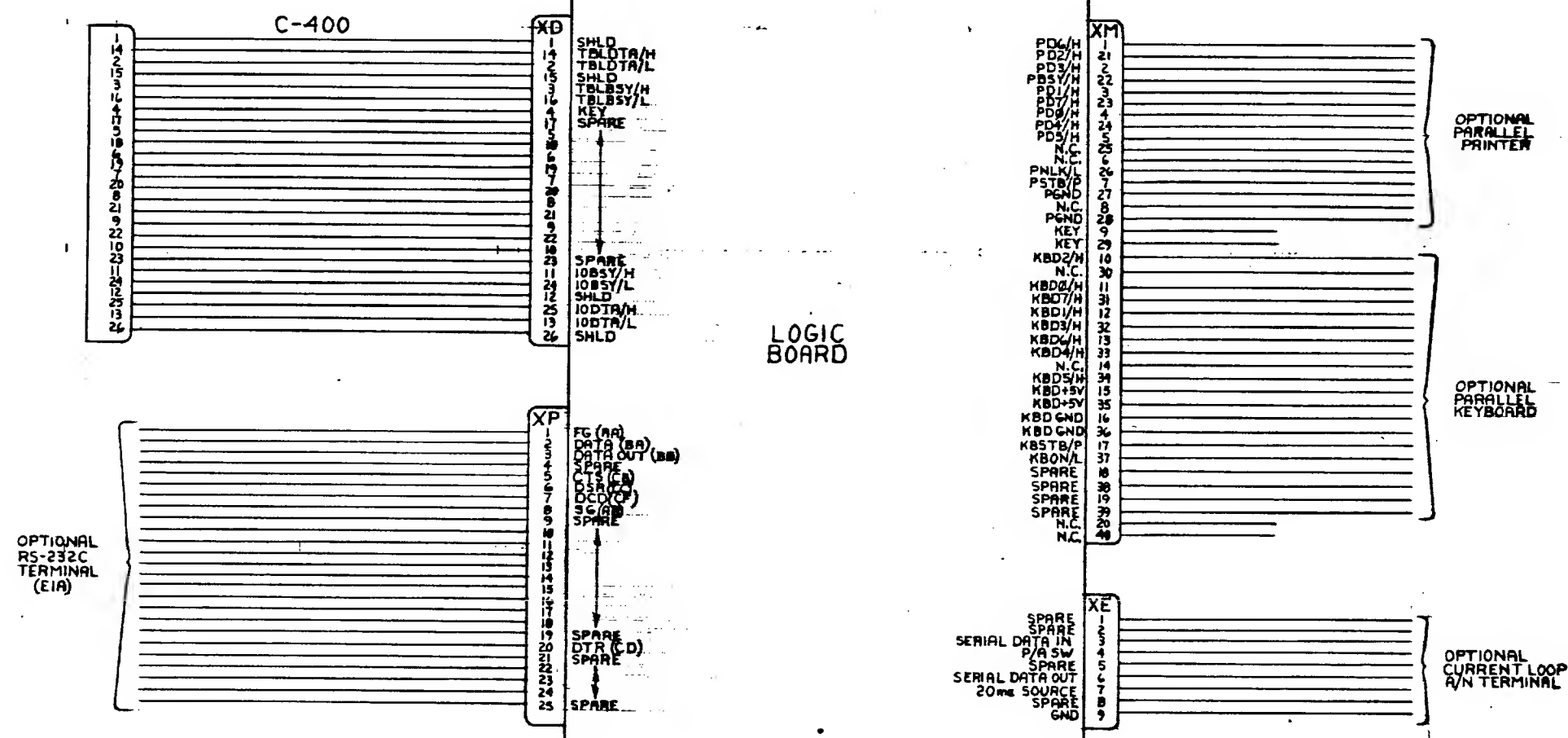


CABLE	ASSY	PAGE	CABLE	ASSY	PAGE
C863	CA20E3063	2	C872	CA20E3072	2
C864	CA20E3064	2	C873	CA20E3073	2
C865	CA20E3065	2	C874	CA20E3074	2
C866	CA20E3066	2	C875	DA20E3075	3
C867	DA20E3067	3	C876	CA20E3076	3
C868	CA20E3068	3	C737	A20E2137	2
C869	CA20E3069	3	C400	CA14E1001	4
C870	DA20E3070	3	TO EIA		4
C871	CA20E3071	2	TO CURRENT LOOP		4
			TO KEYBOARD/PRINTER		4

BUS EXT. C.737
FOR MAINTENANCE
DEVELOPMENT PURPOSES
ONLY

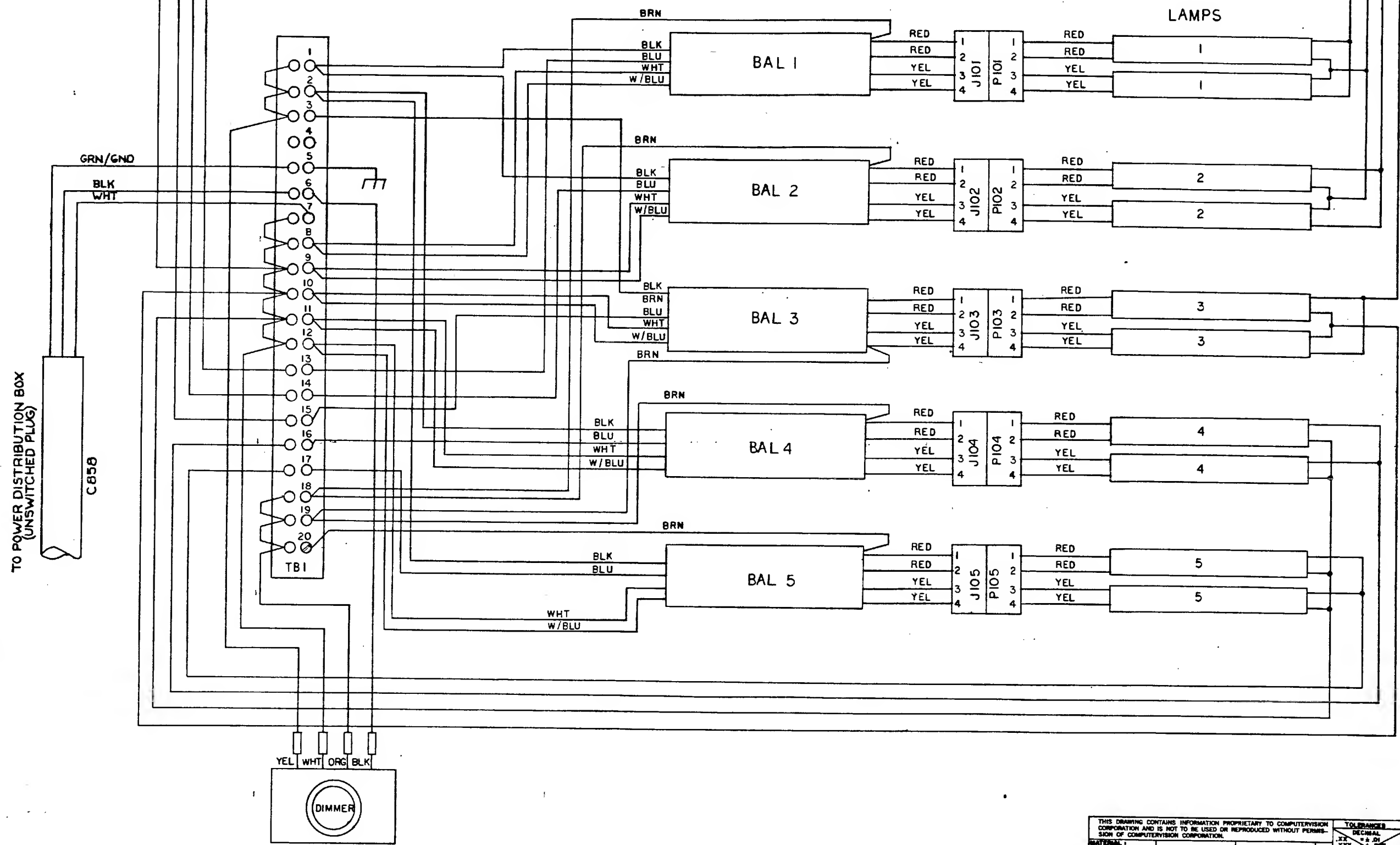
THIS DRAWING CONTAINS INFORMATION PROPRIETARY TO COMPUTERVISION CORPORATION AND IS NOT TO BE USED OR REPRODUCED WITHOUT PERMISSION OF COMPUTERVISION CORPORATION.		TOLERANCES DECIMAL XXX ± .01 XXX ± .005 FRACTIONAL 3/64 ANGULAR ± 1°00'		B ECO #2440 A REL ECO 2270 TITLE SYSTEM INTER- CONNECTION DIAGRAM DATE 3-77 APP'D DATE	
PART NUMBER	NETT ASSEMBLY	QTY	SCALE	NONE	REV A - REV 1
COMPUTERVISION CORP. SOUTH AVENUE BURLINGTON, MASS. 01803			REMOVE ALL BURRS AND SHARP EDGES		SHEET 1 OF 4 SHEETS





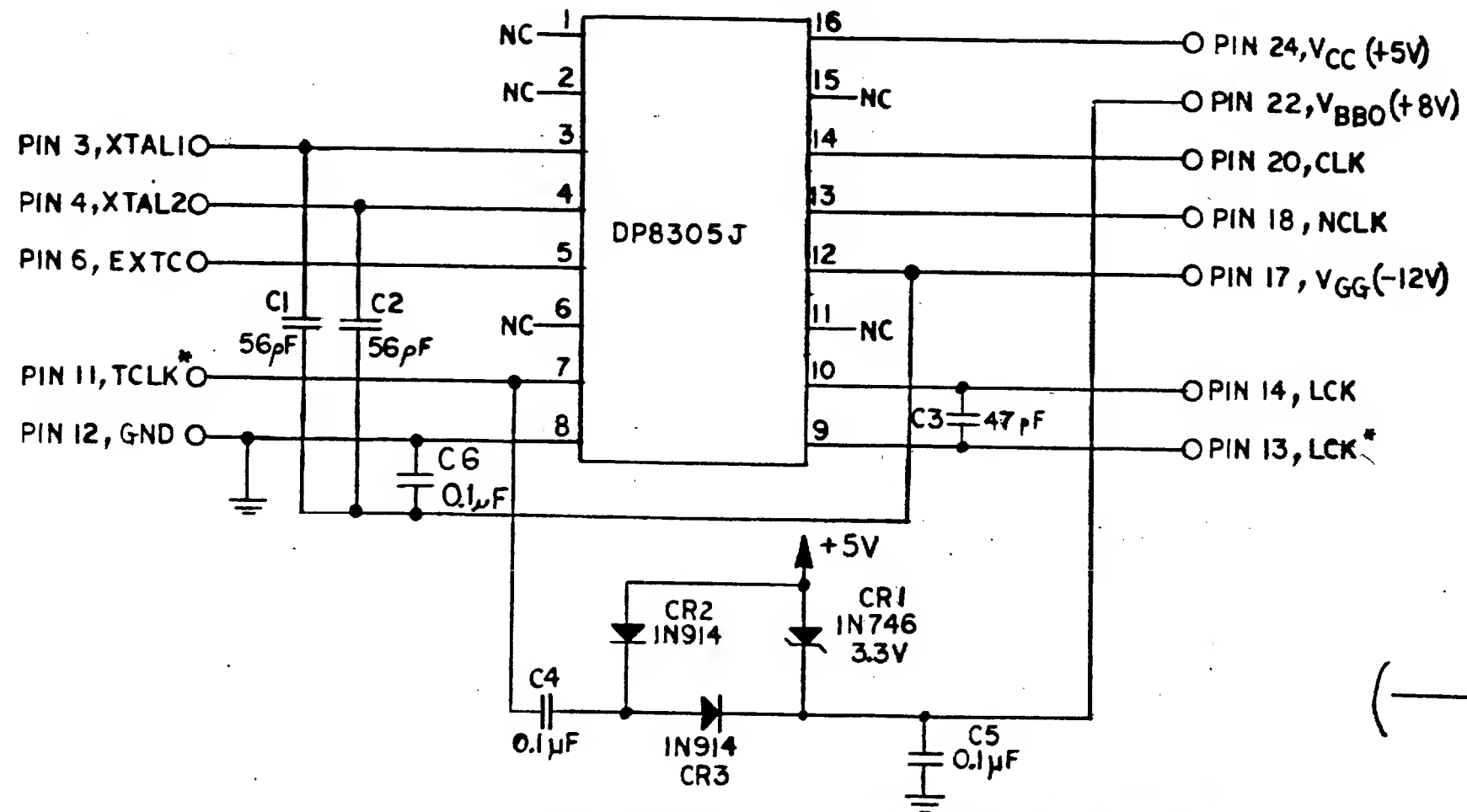
DS20E3002
B

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MATERIAL				BRND. DAVIDSON 11-77				SEE SHEET 1			
FINISH				CHK				TITLE			
SEE SHEET 1				ENGR				CVD INTERCONNECTION DIAGRAM			
PART NUMBER				NEXT ASSEMBLY				QTY			
COMPUTERVISION CORP. SOUTH AVENUE BURLINGTON, MASS. 01803				SCALE NONE				SHEET 4 OF 4 SHEETS			



DS20E2196
B

THIS DRAWING CONTAINS INFORMATION PROPRIETARY TO COMPUTERVISION CORPORATION AND IS NOT TO BE USED OR REPRODUCED WITHOUT PERMISS.		TOLERANCES			
DRAWN: <i>CM</i>		DECIMAL			
CHECKED: <i>CM</i>		FRACTIONAL			
PART NUMBER		NEXT ASSEMBLY		QTY	
A20M3015		L20X3007		L20X2007	
A20M2011					
COMPUTERVISION CORP.		BURLINGTON, MASS. 01803			
B ECO #2440		A REL ECO 2065		TITLE	
SCHEMATIC DIAGRAM		CVD BACK LIGHTING		NONE	
DS20E2196		SHEET 1 OF 1		DATE	



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MATERIAL:				DECIMAL			
L				.XX = ± .01			
				.XXX = ± .005			
				.XXXX = ± .0010			
				FRACTIONAL = 1/64			
				ANGULAR = ± 1°00'			
FINISH:				DRN R GETZ 8/77		C	
L20X3007 1				CHK RUT 8/77		B ECO #2597 RUT 4/78	
L20X2007 1				ENGR RUT 9/77		A ECO #2439 RUT 8/77	
DA20E3050 REF				PROJ RUT 8/77		SYM REVISION DESCRIPTION APPD DATE	
PART NUMBER NEXT ASSEMBLY QTY				MFG 8/20 8/77		TITLE SCHEMATIC DIAGRAM CVD TIMING BOARD	
COMPUTERVISION CORP.				SIGNATURE DATE		SCALE NONE	
201 Burlington Road				REMOVE ALL BURRS AND SHARP EDGES		UNIT WT. RUT	
Bedford, Massachusetts 01730						DWS NO. BS20E3097	
						SHEET 1 OF 1 SHEETS	